

FIG. 1

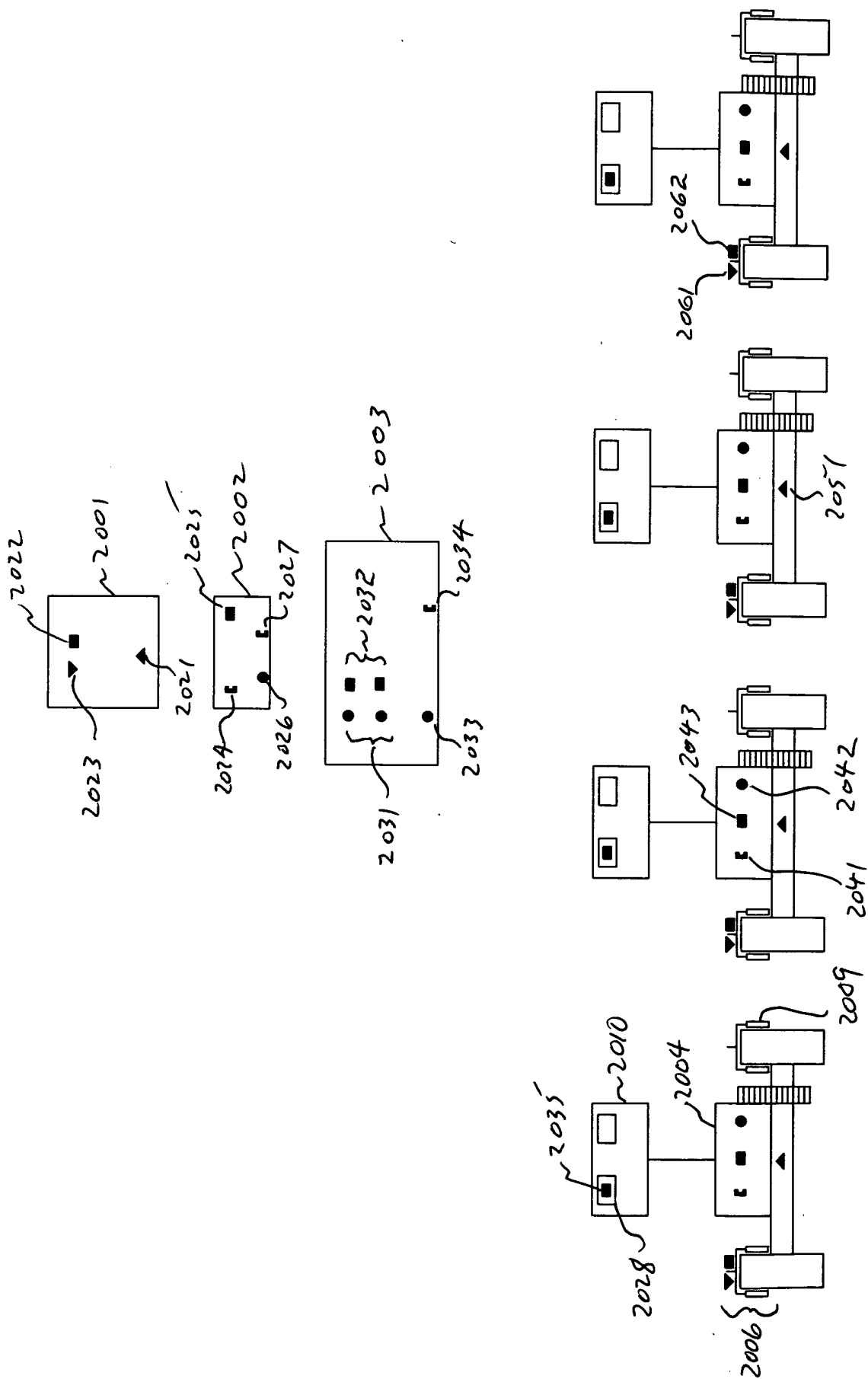


FIG. 2

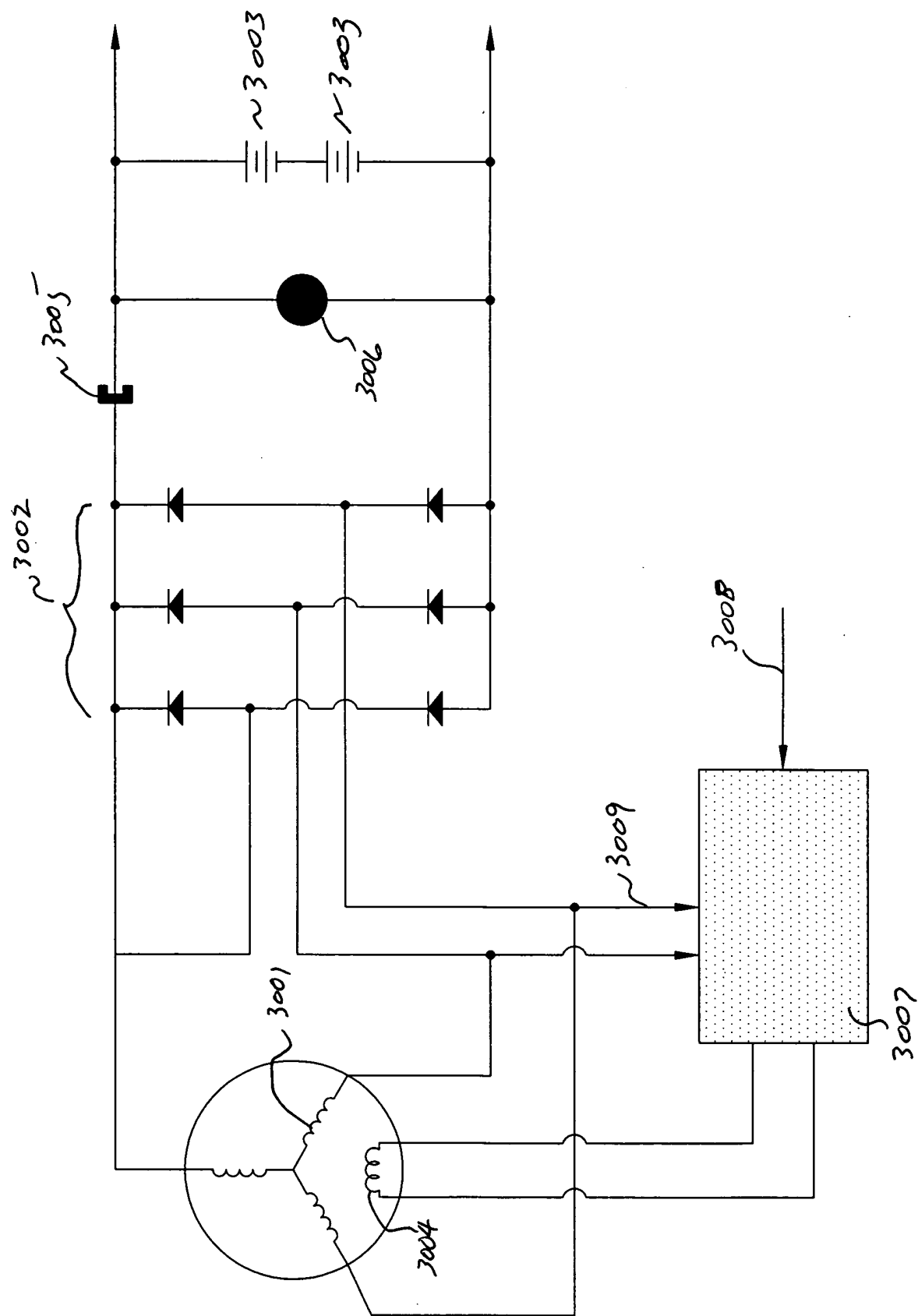
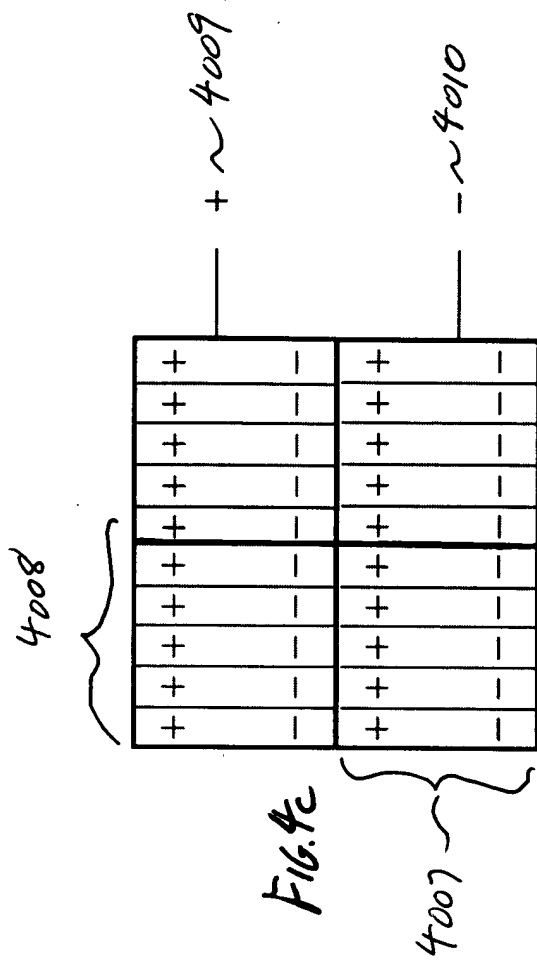
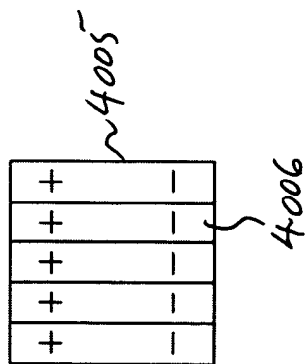
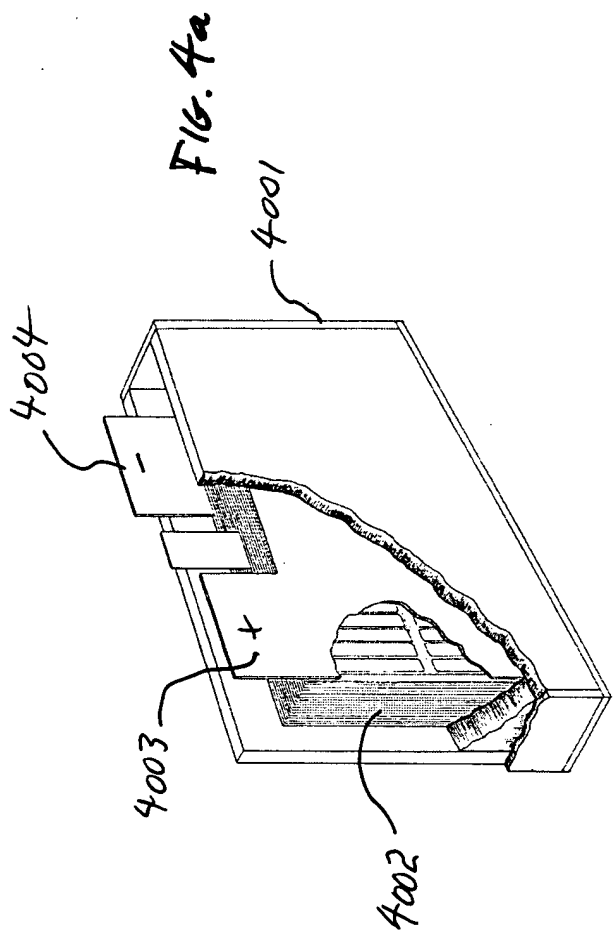


FIG. 3



**FIG. 4**

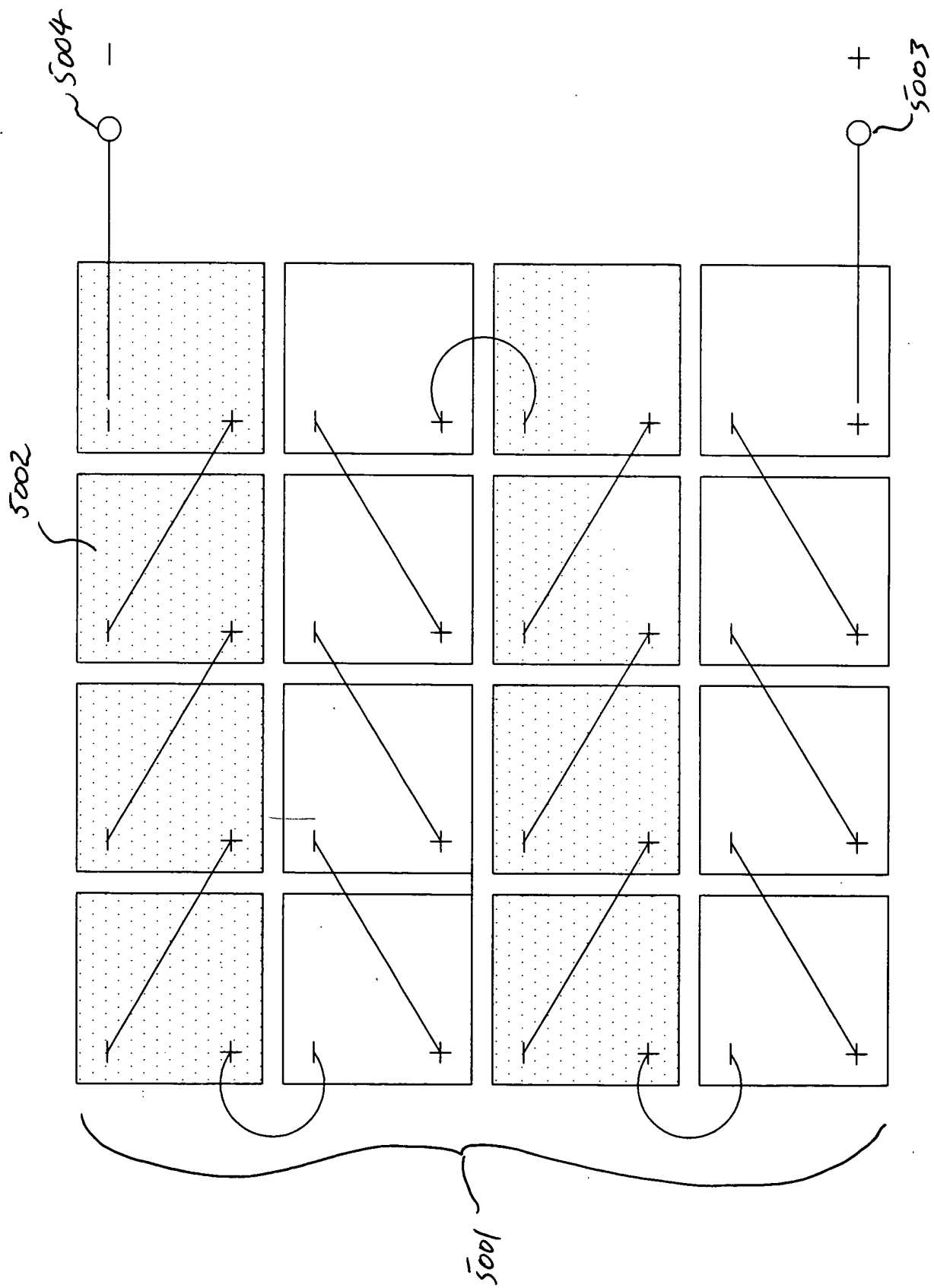


FIG. 5

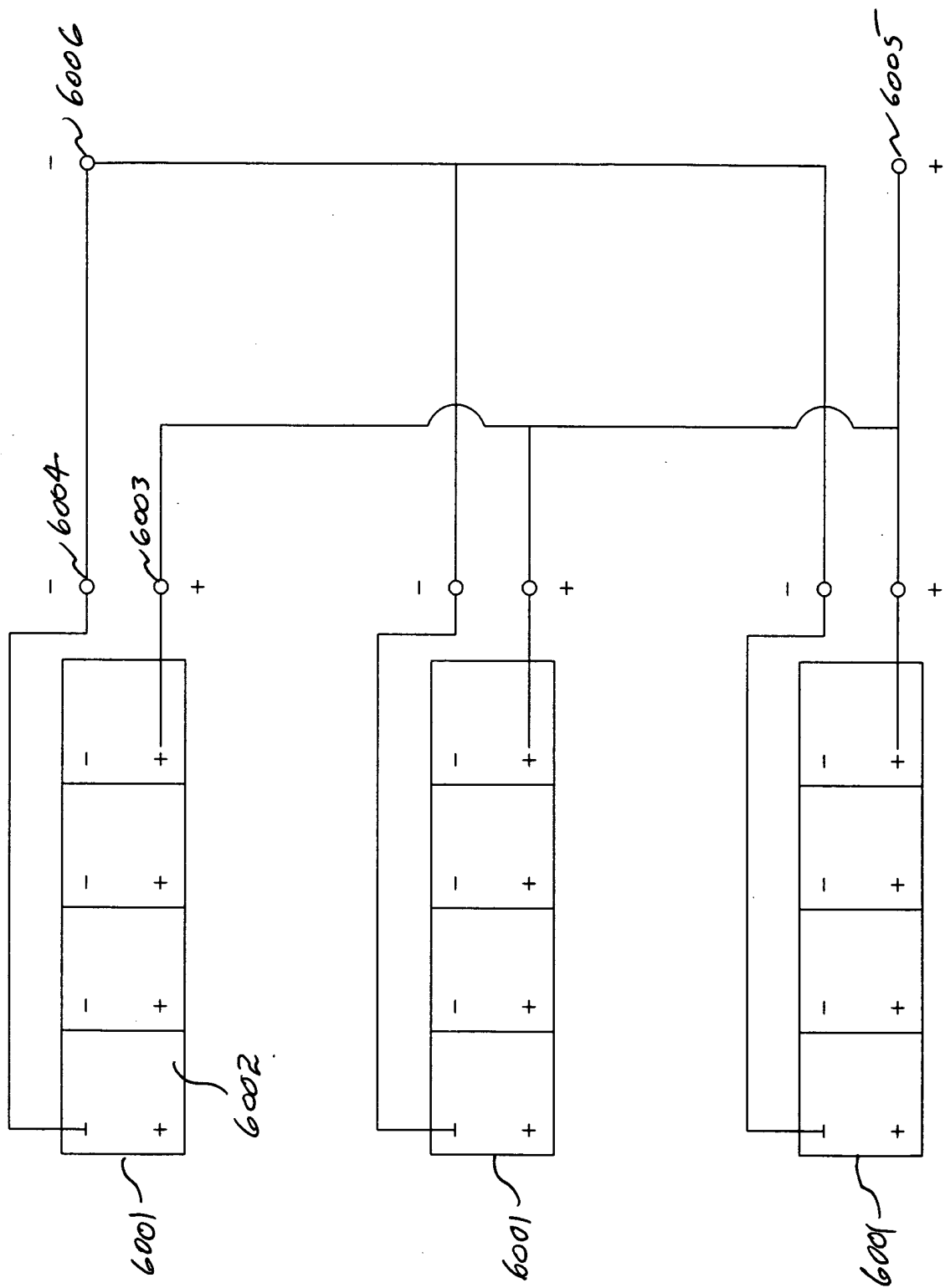


FIG. 6

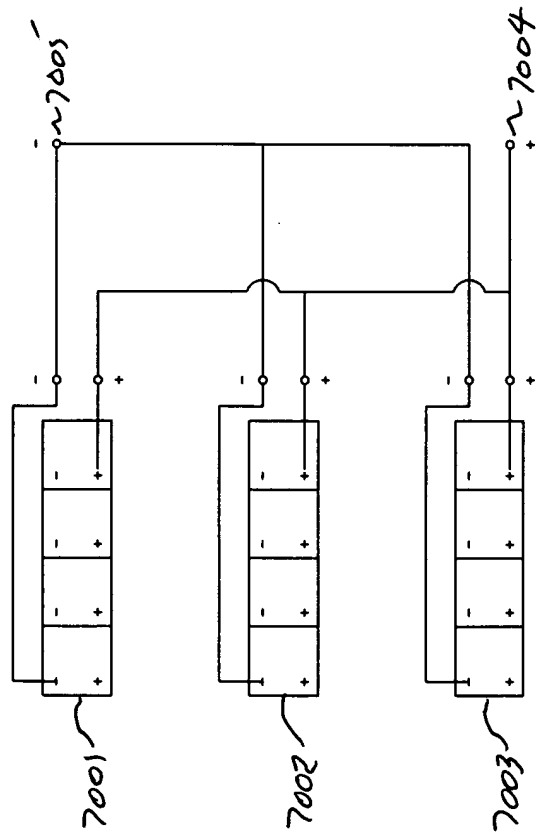


FIG. 7a

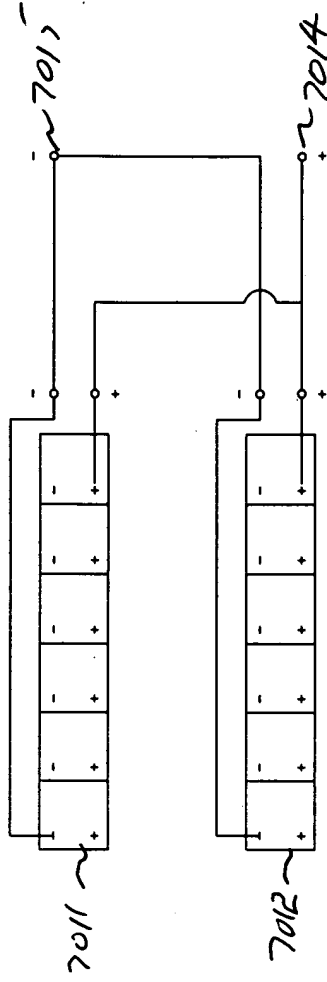


FIG. 7b

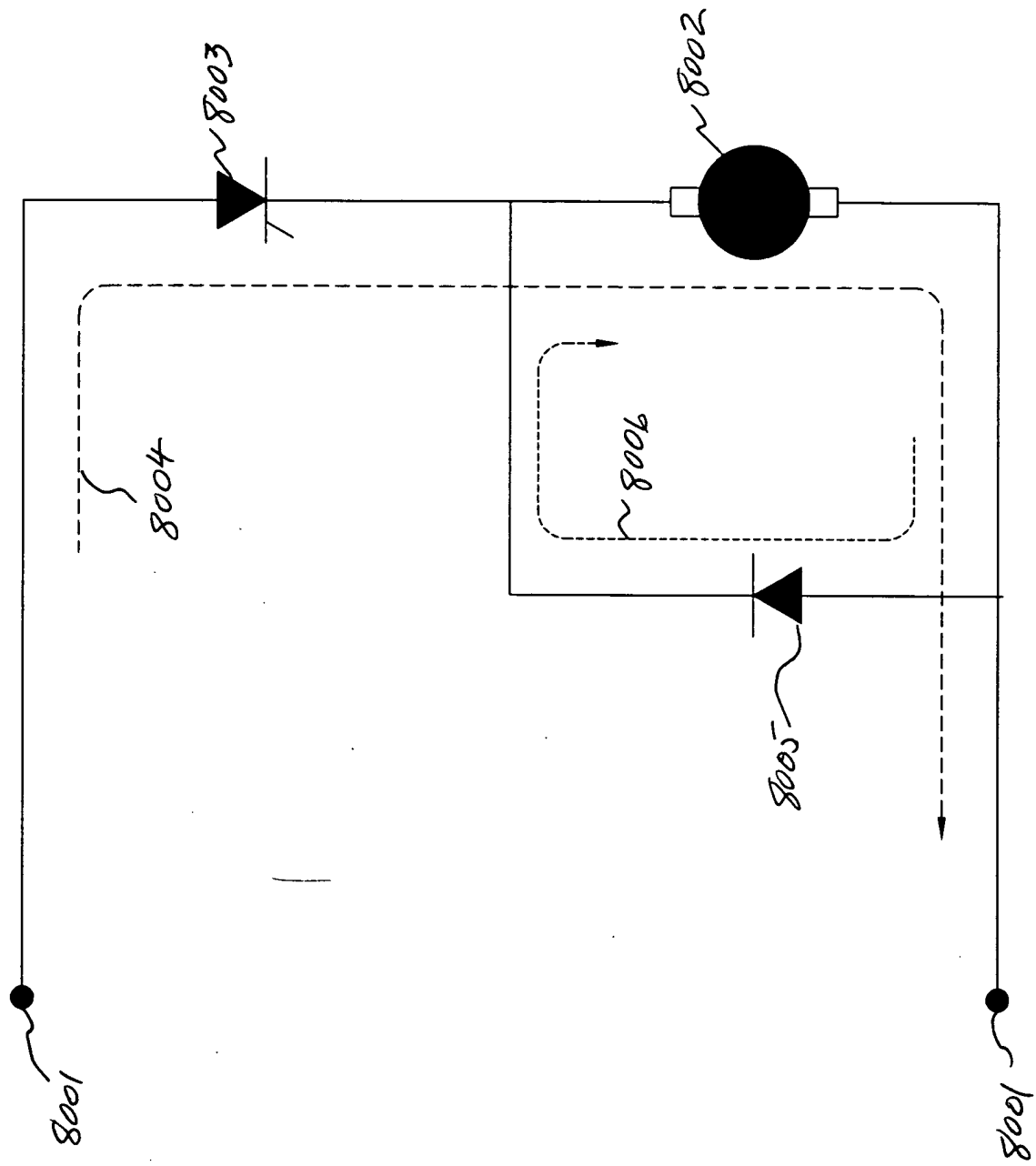


FIG. 8



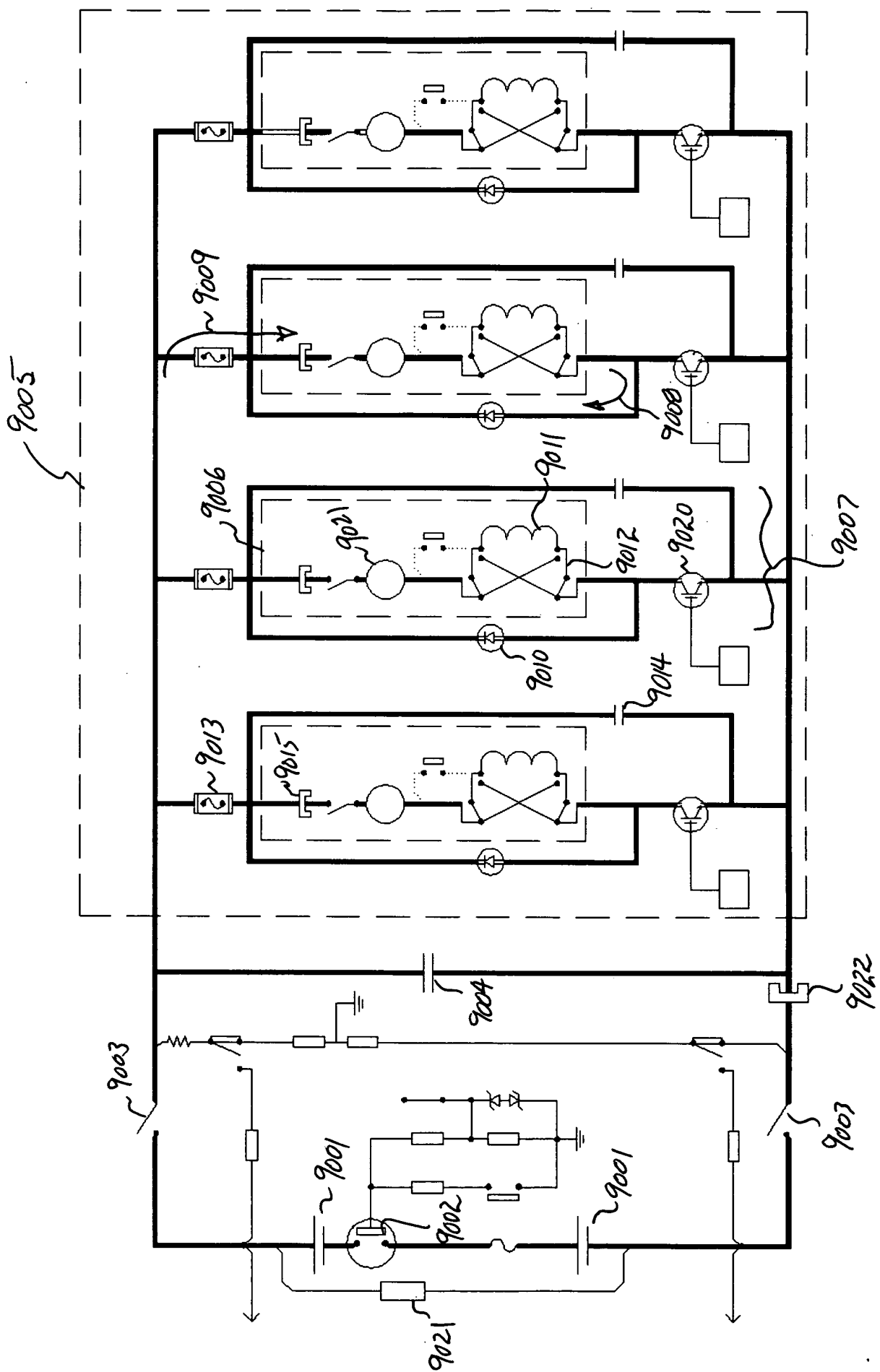


FIG. 9

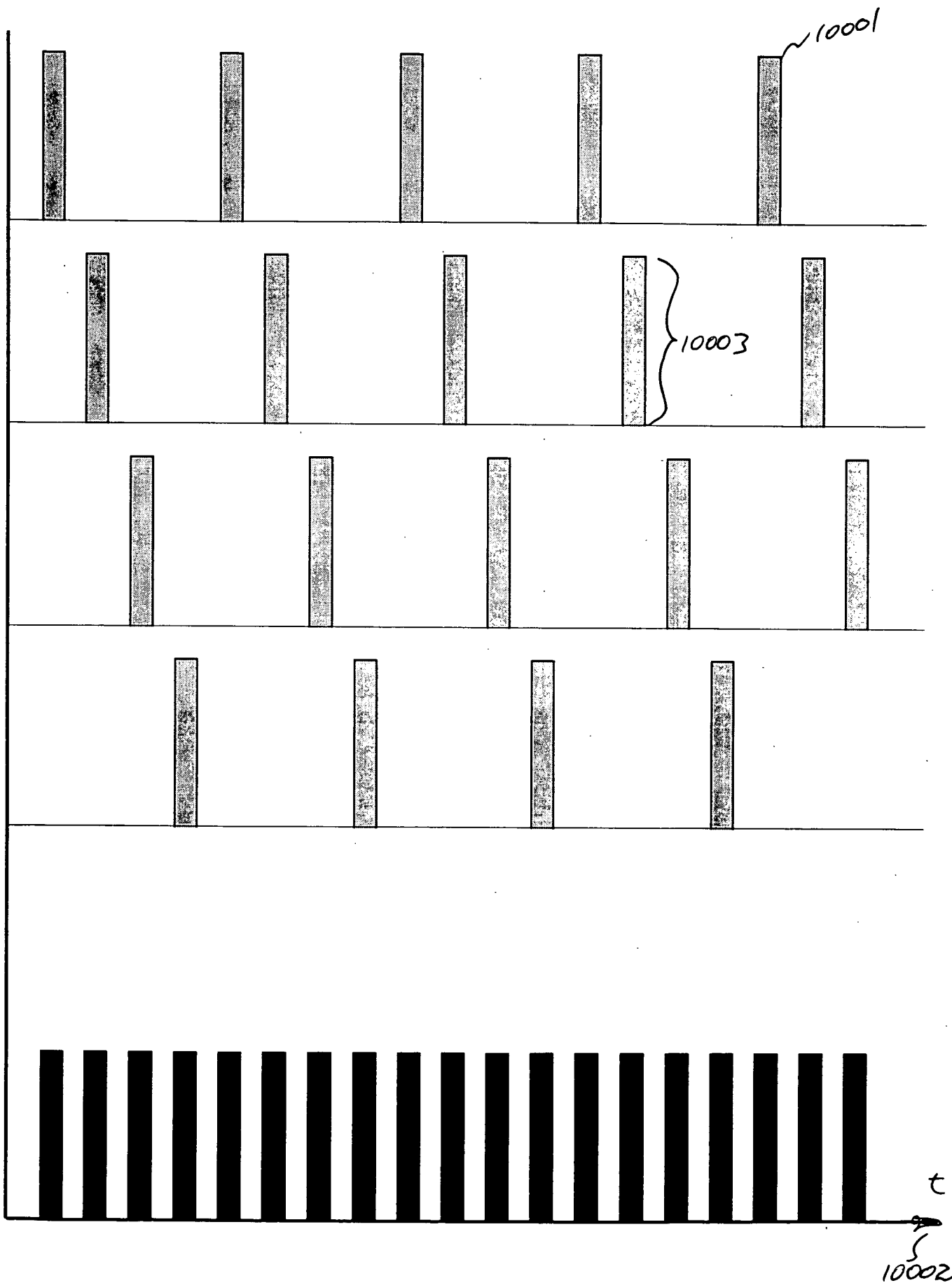


FIG. 10

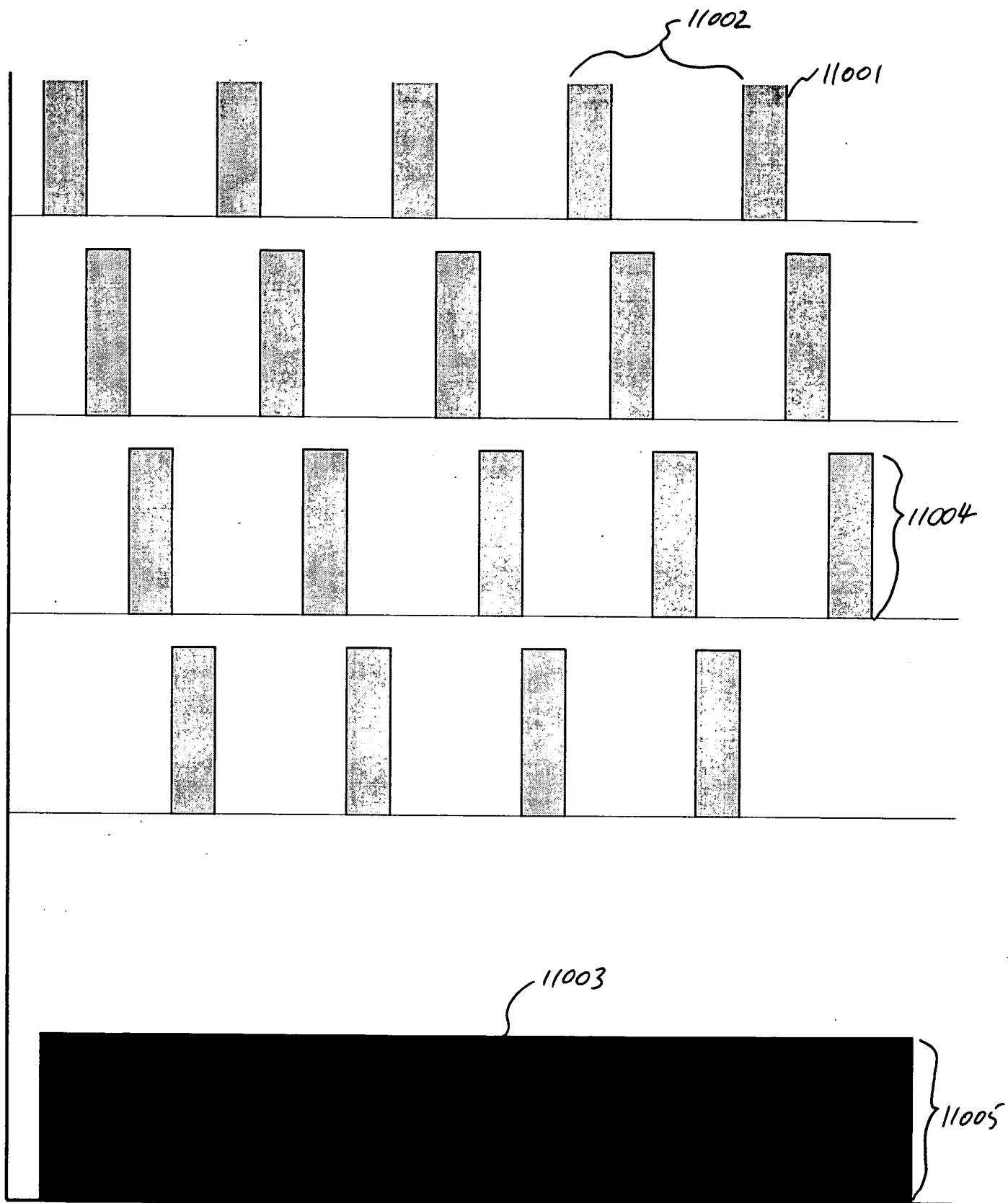


FIG. 11

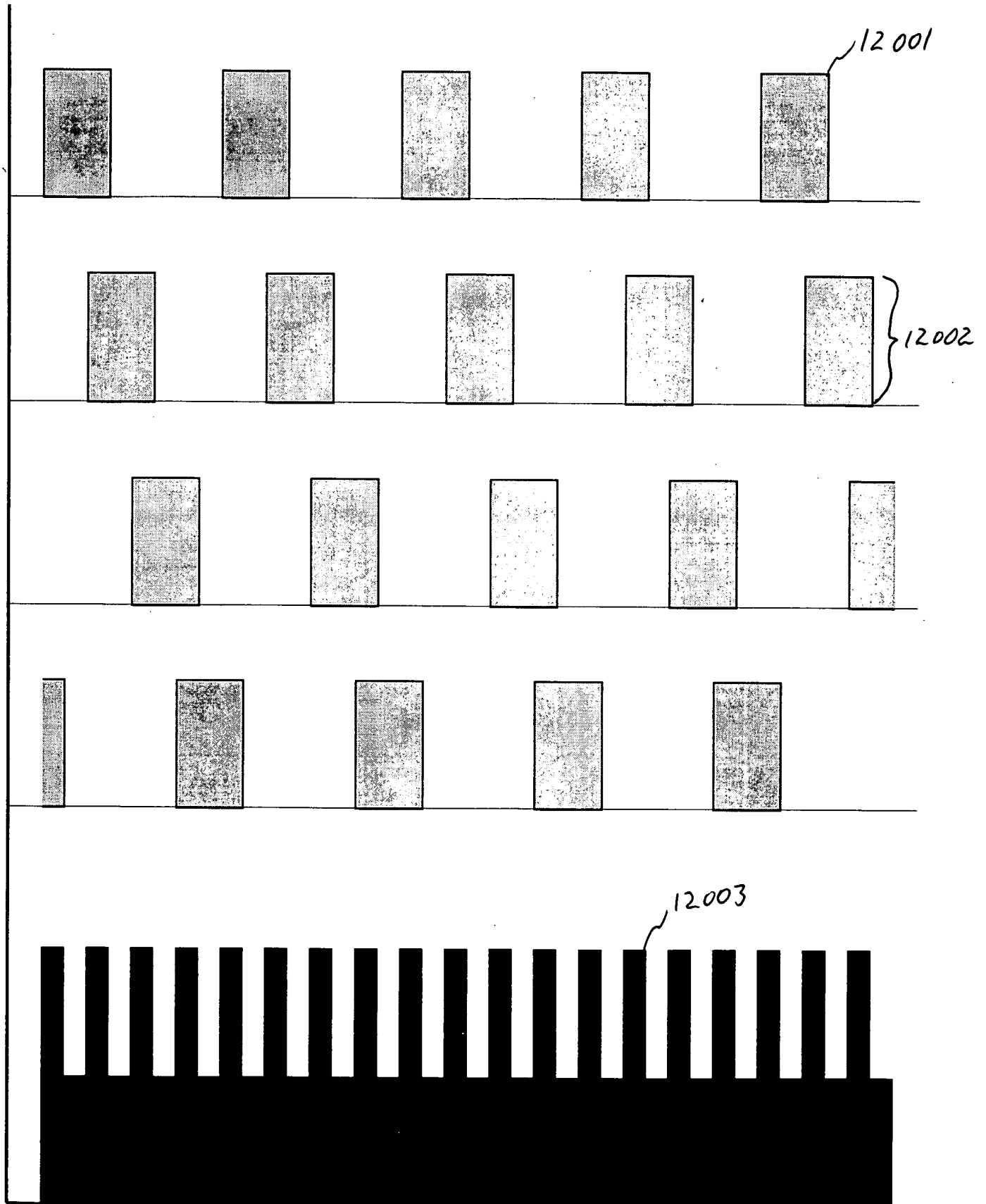


FIG. 12

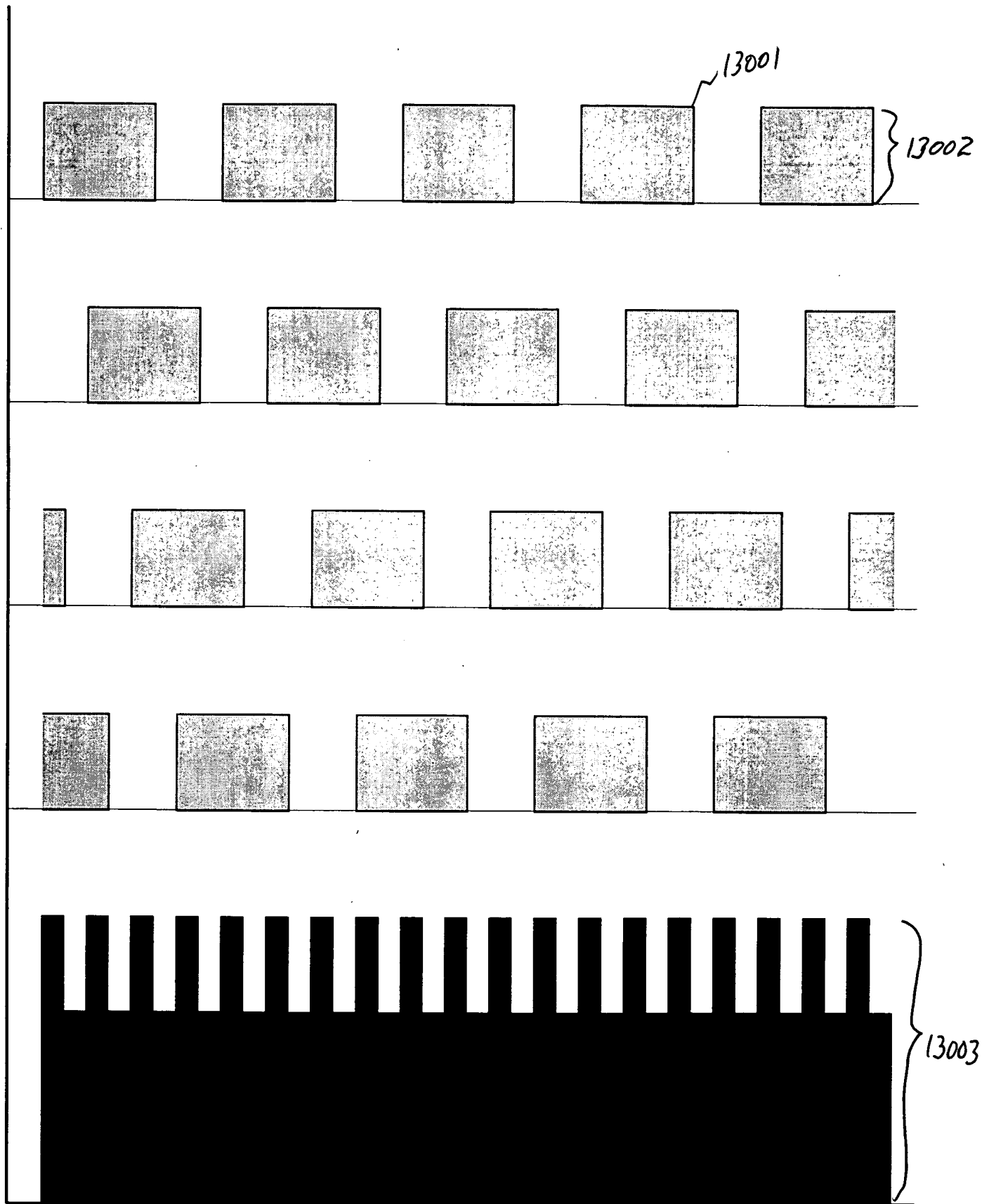


FIG. 13

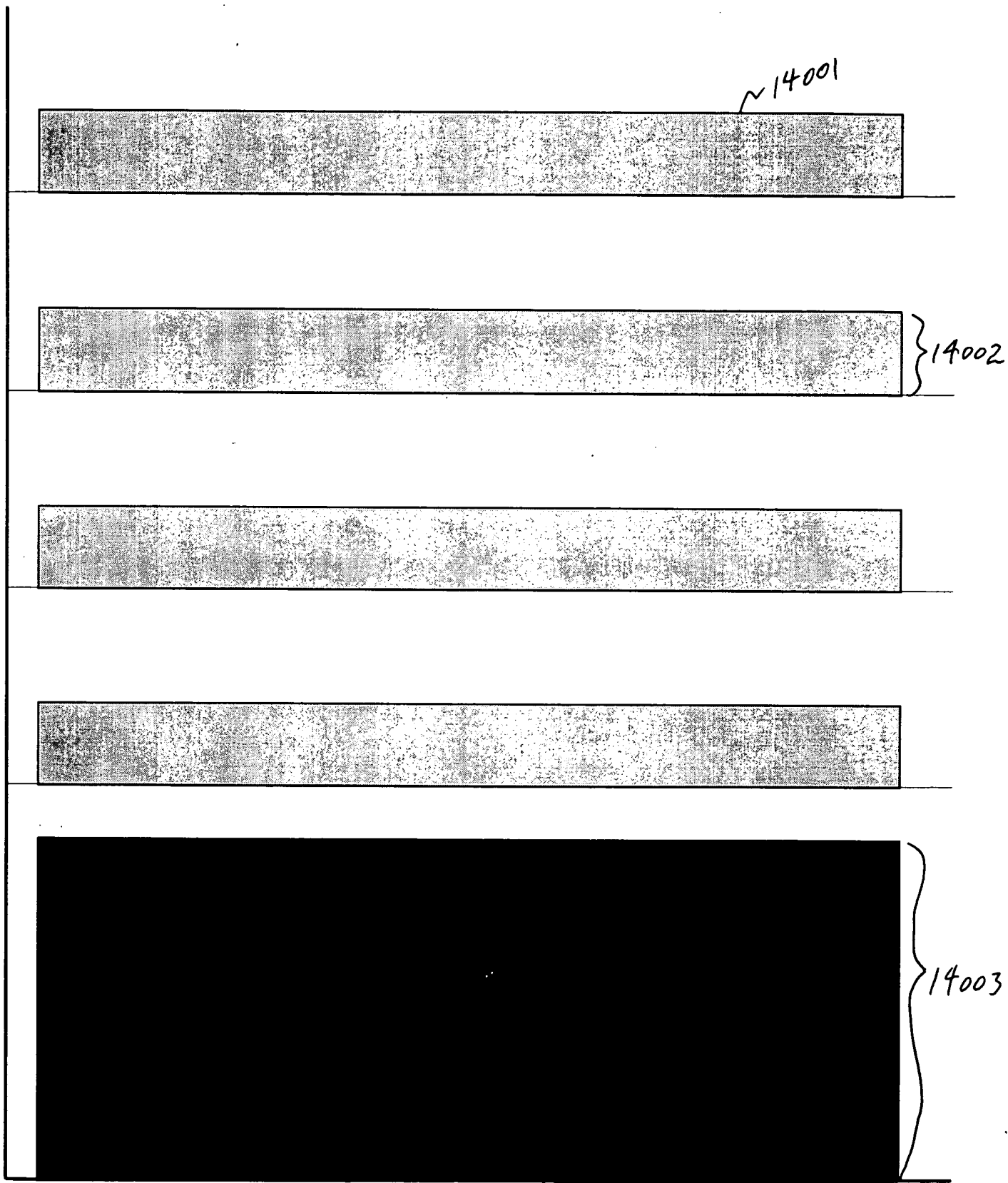


FIG. 14

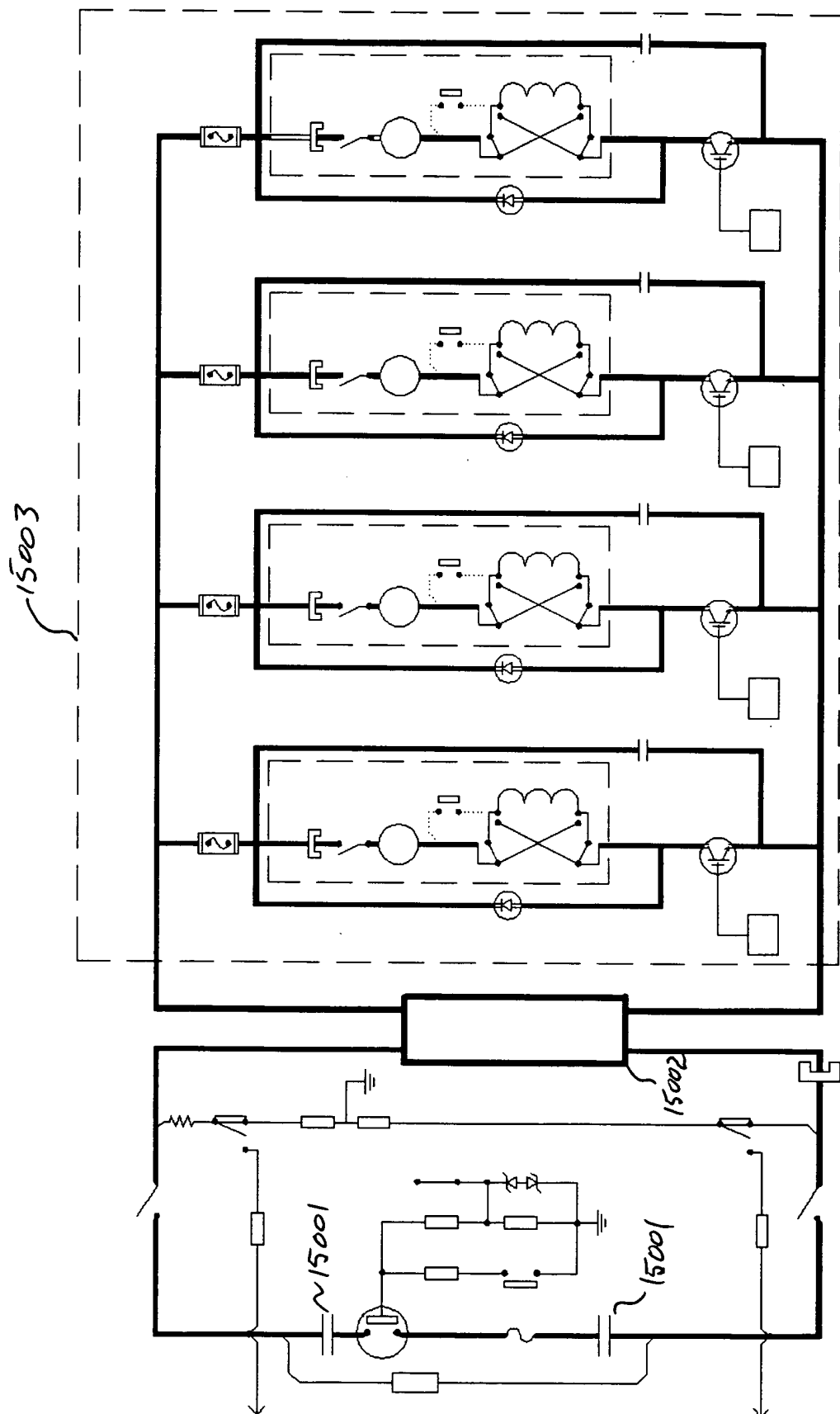


FIG. 15

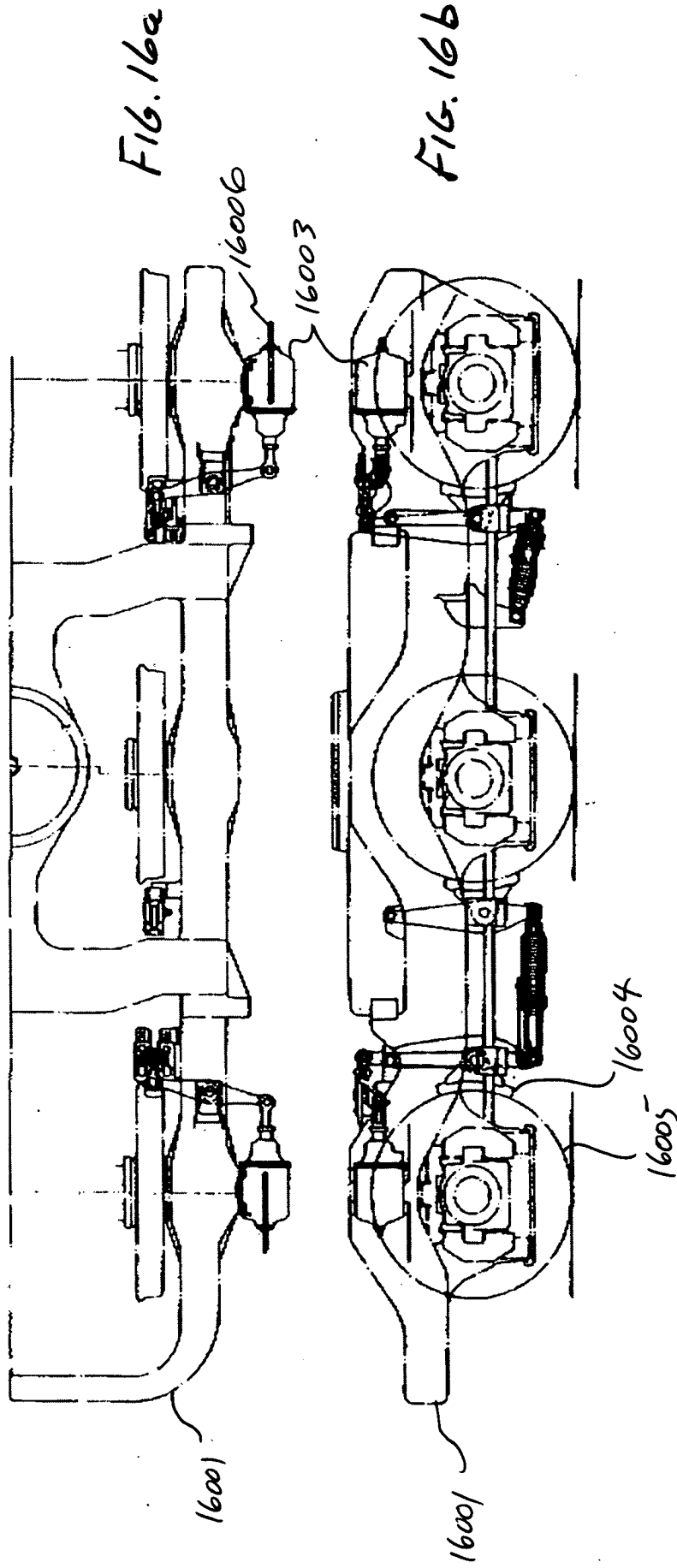


FIG. 16



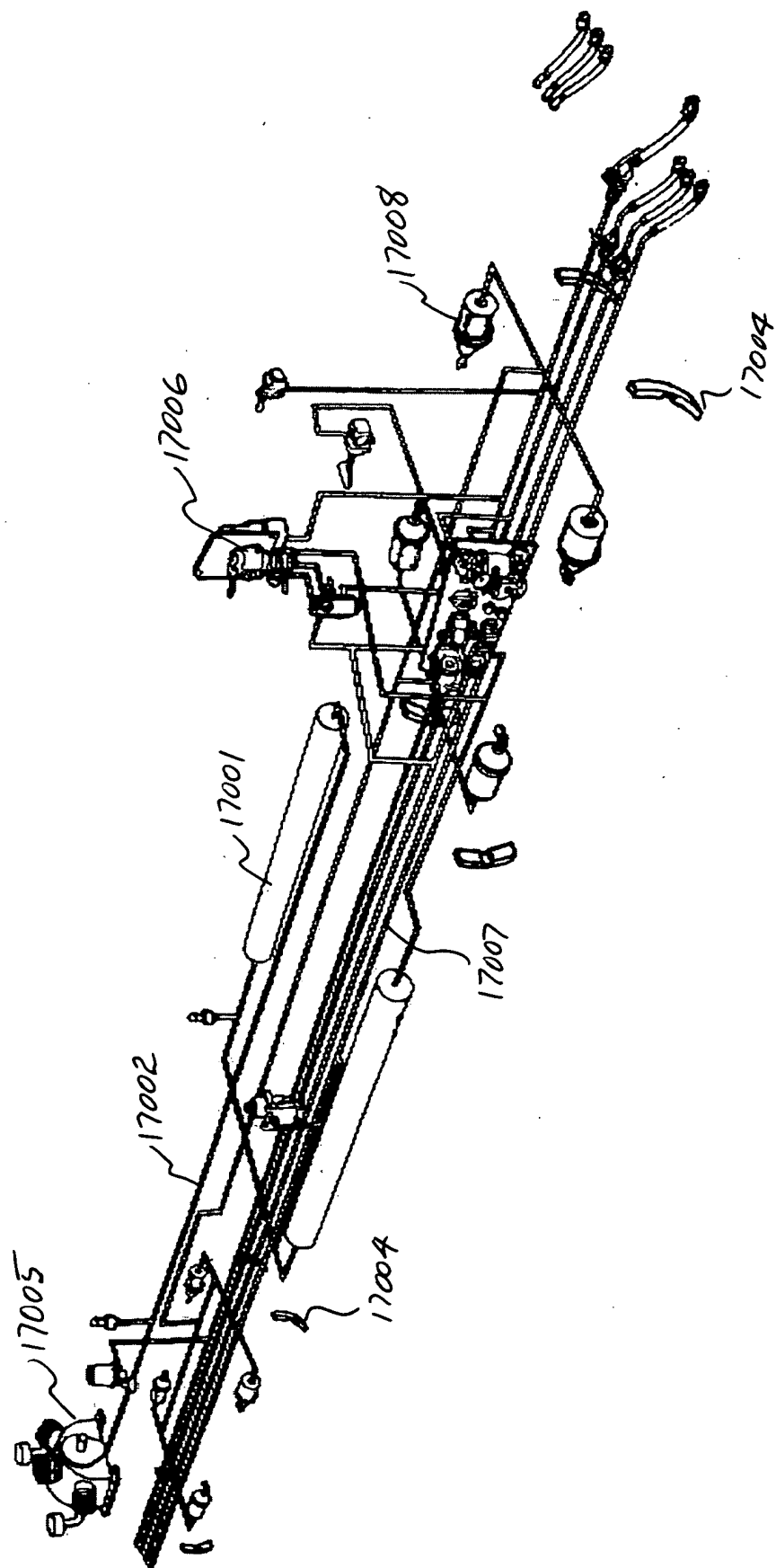


FIG. 17

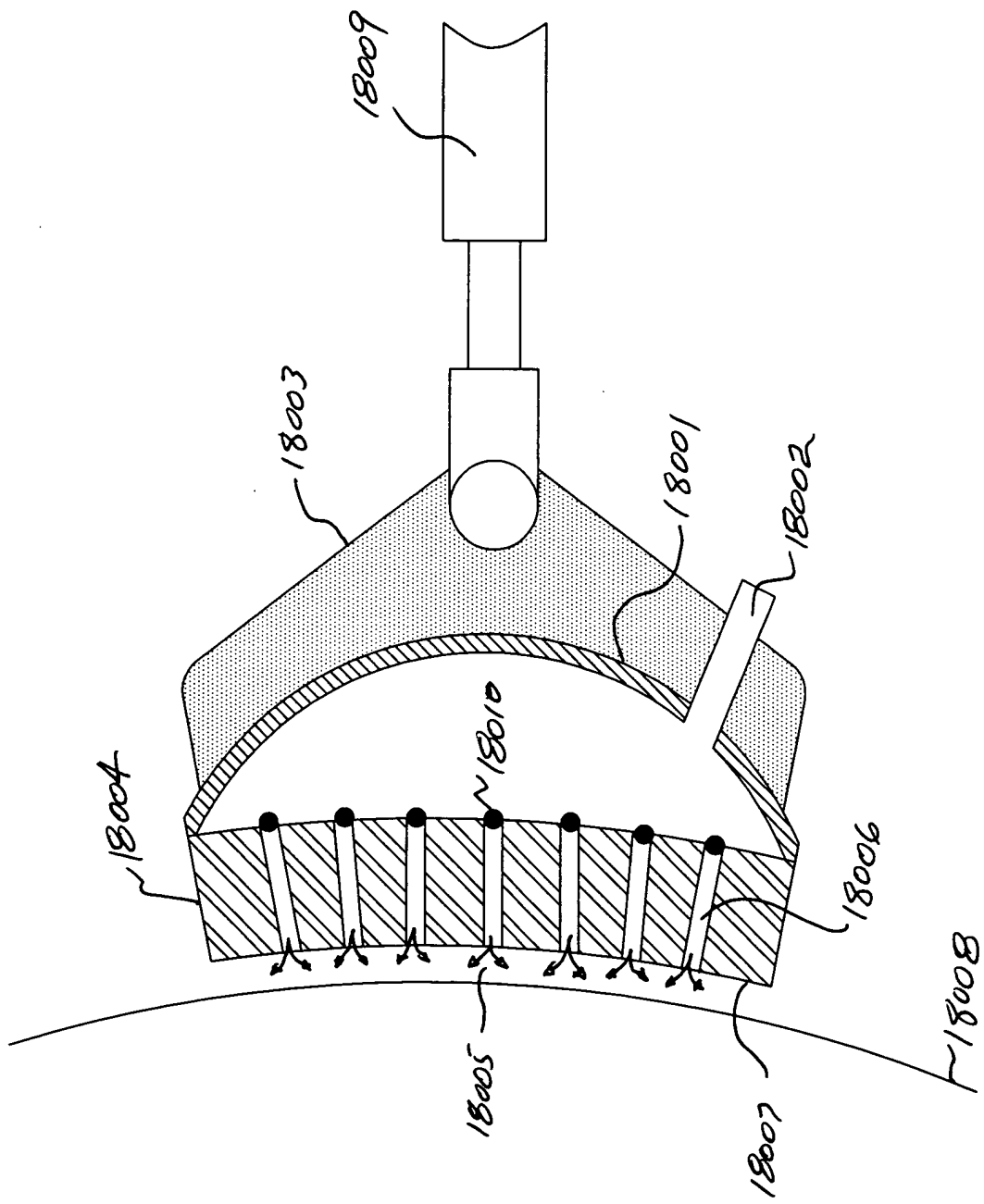


FIG. 18

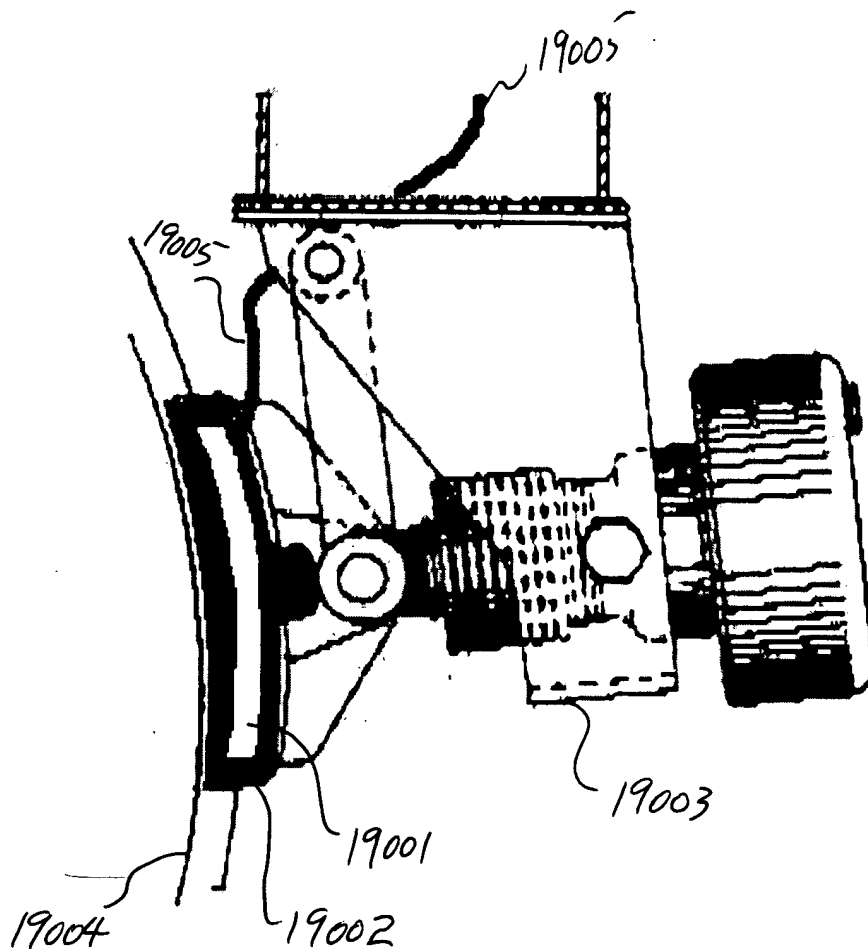


FIG. 19

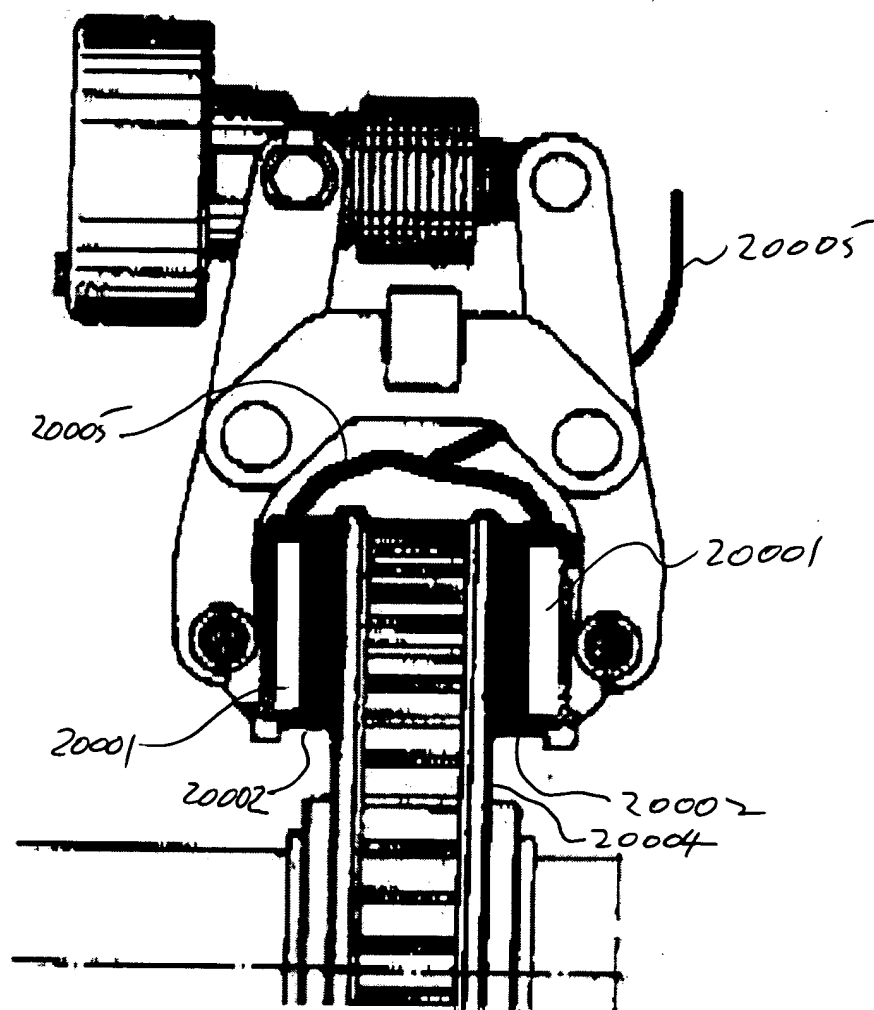


FIG. 20

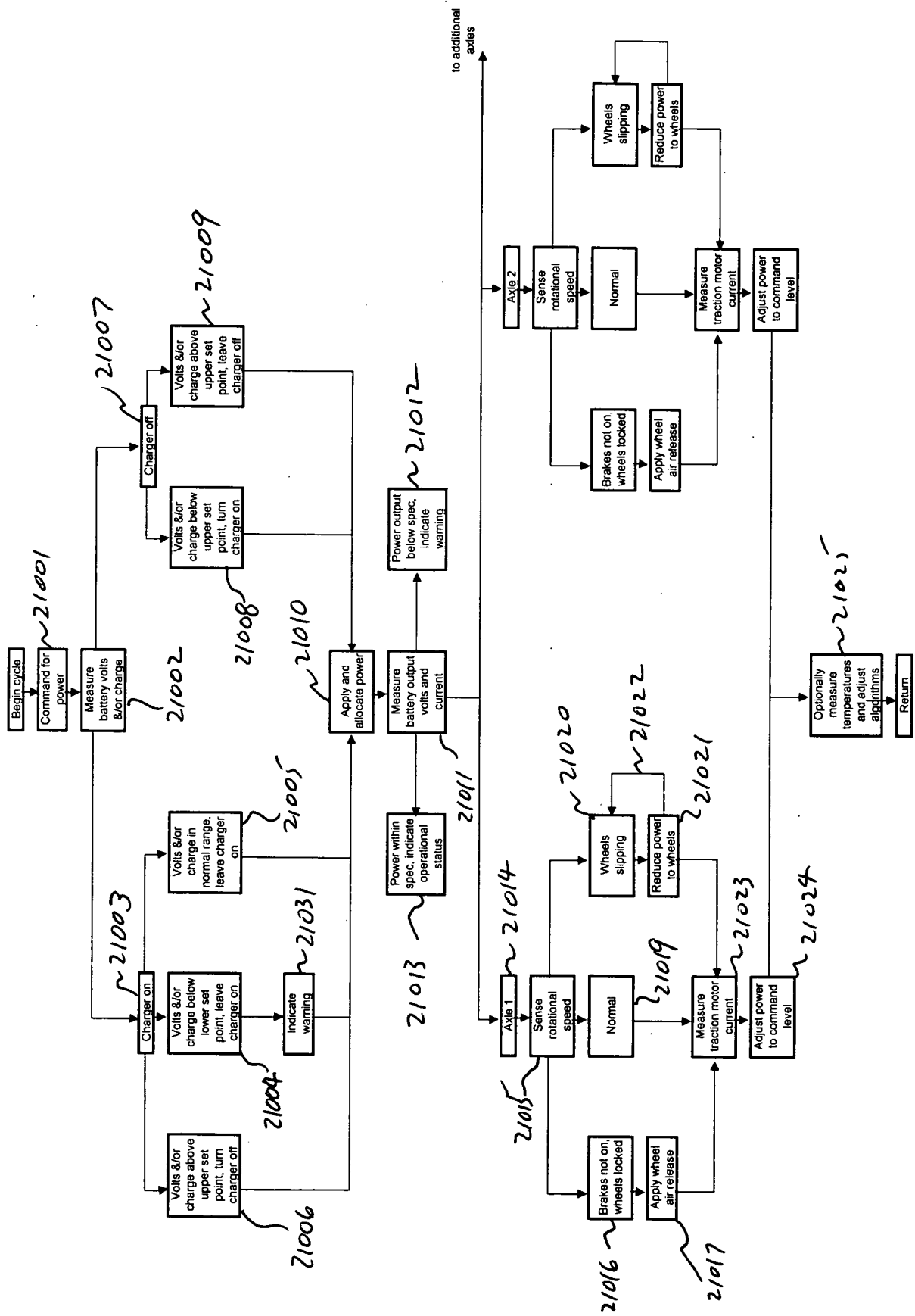


FIG. 21

FIG. 22a

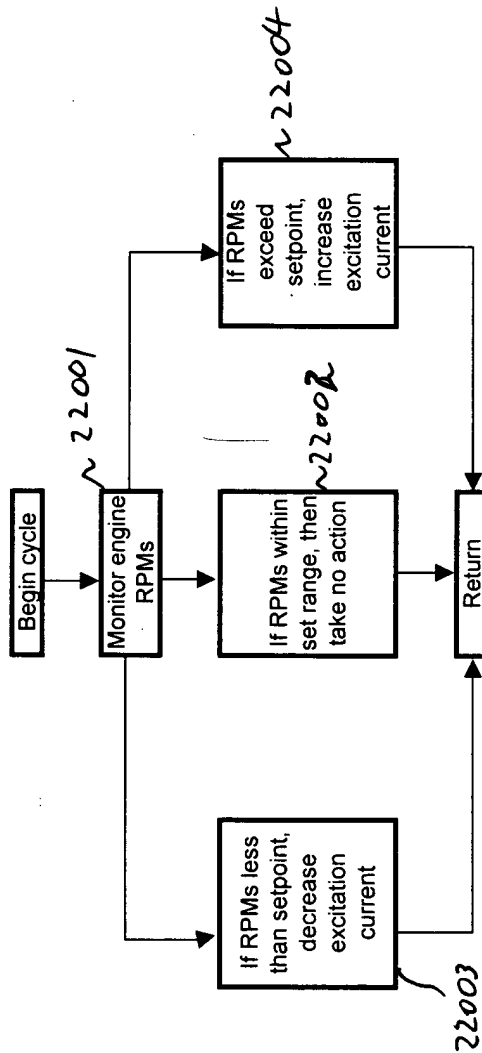


FIG. 22b

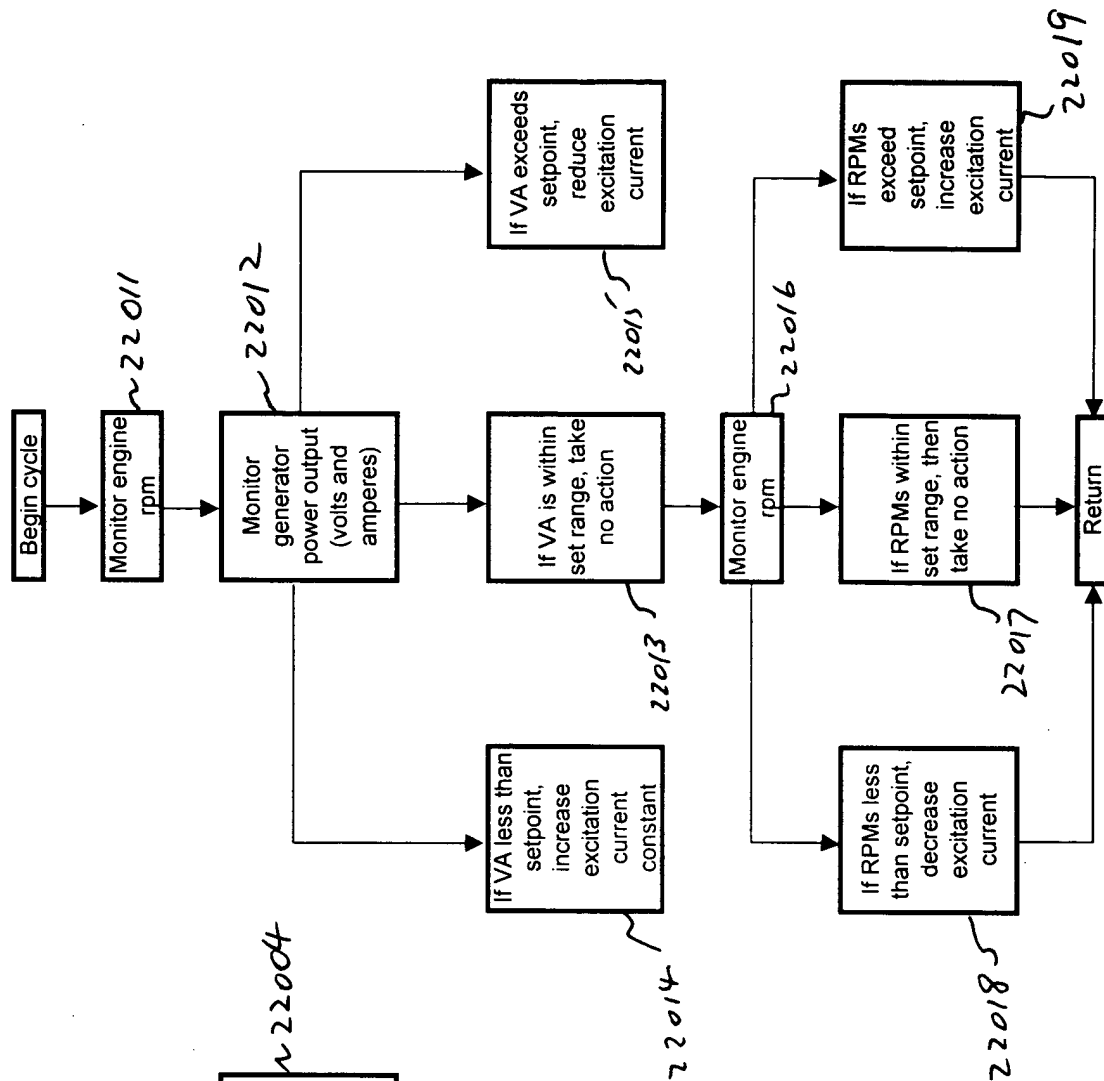


FIG. 22

FIG. 23a

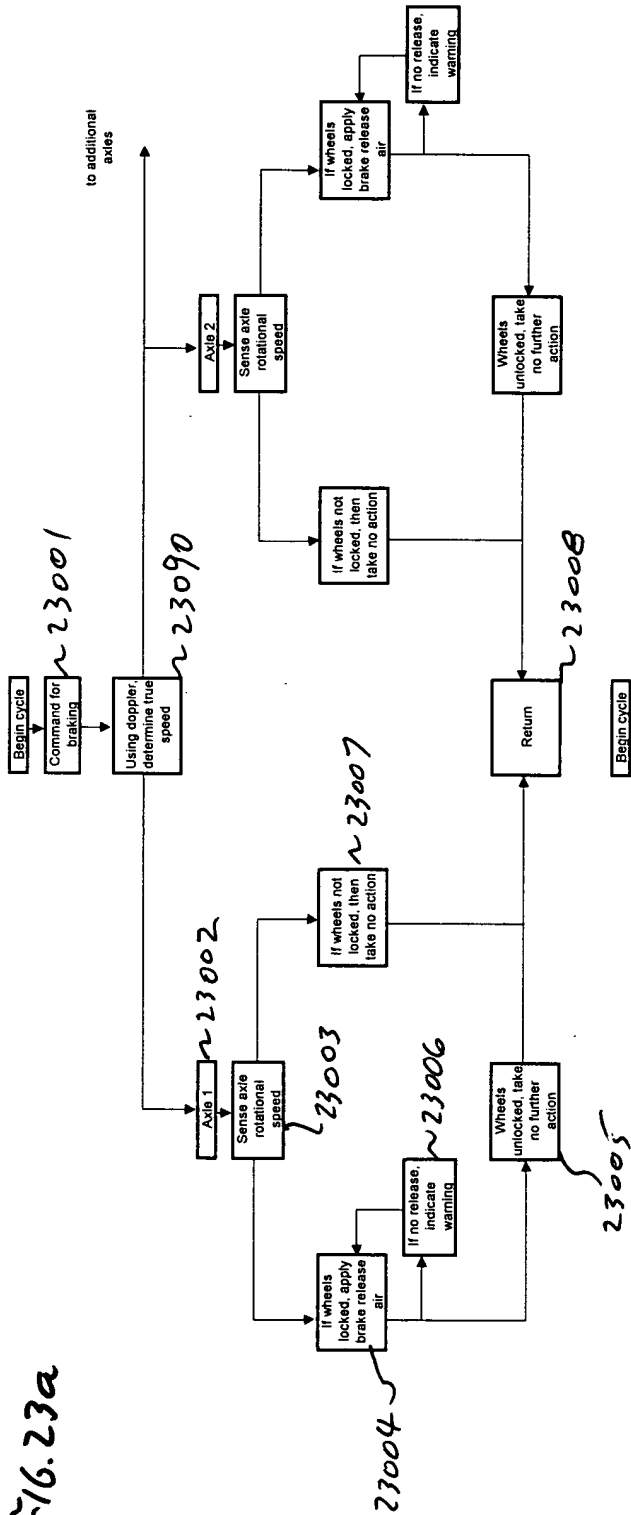


FIG. 23b

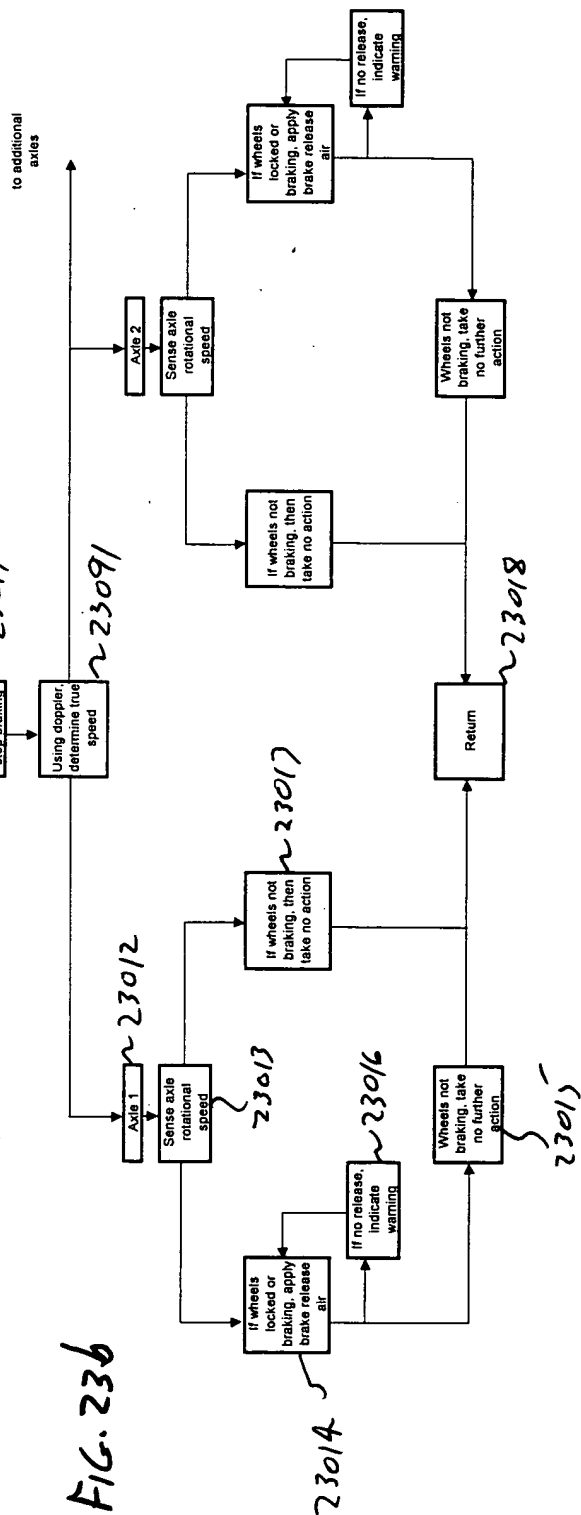


FIG. 23

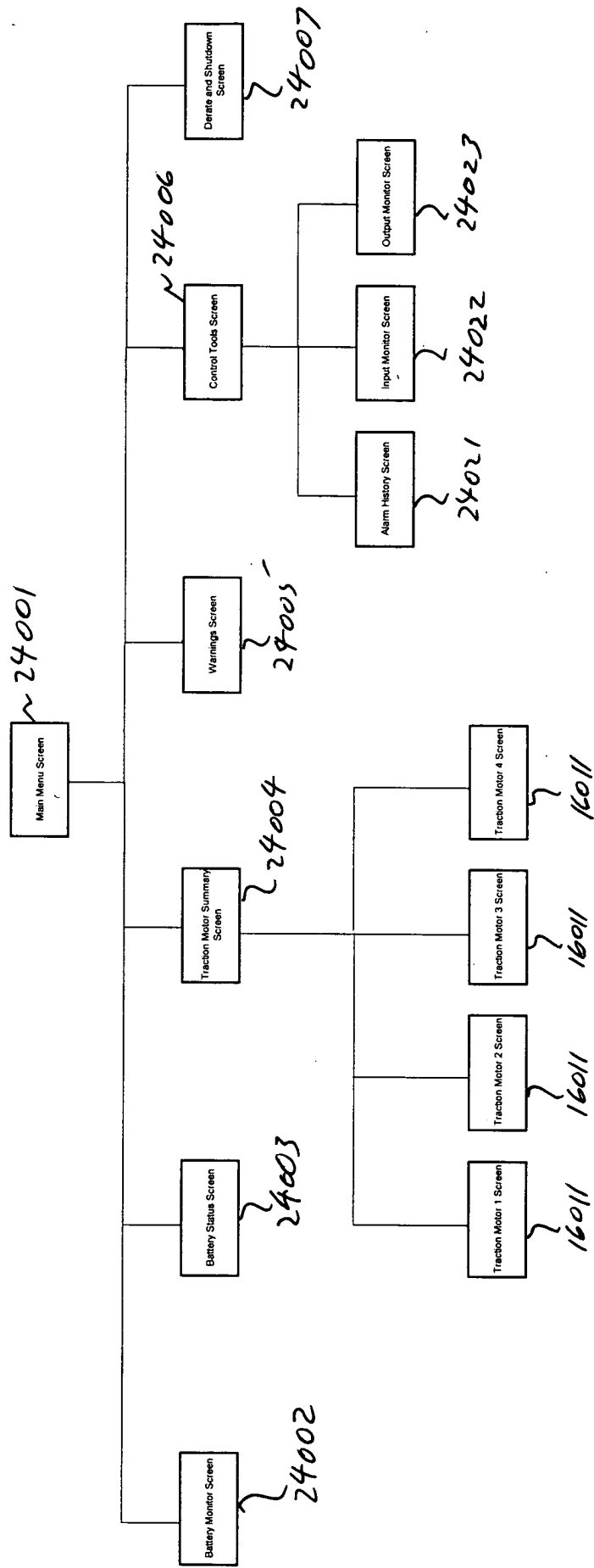


FIG. 24



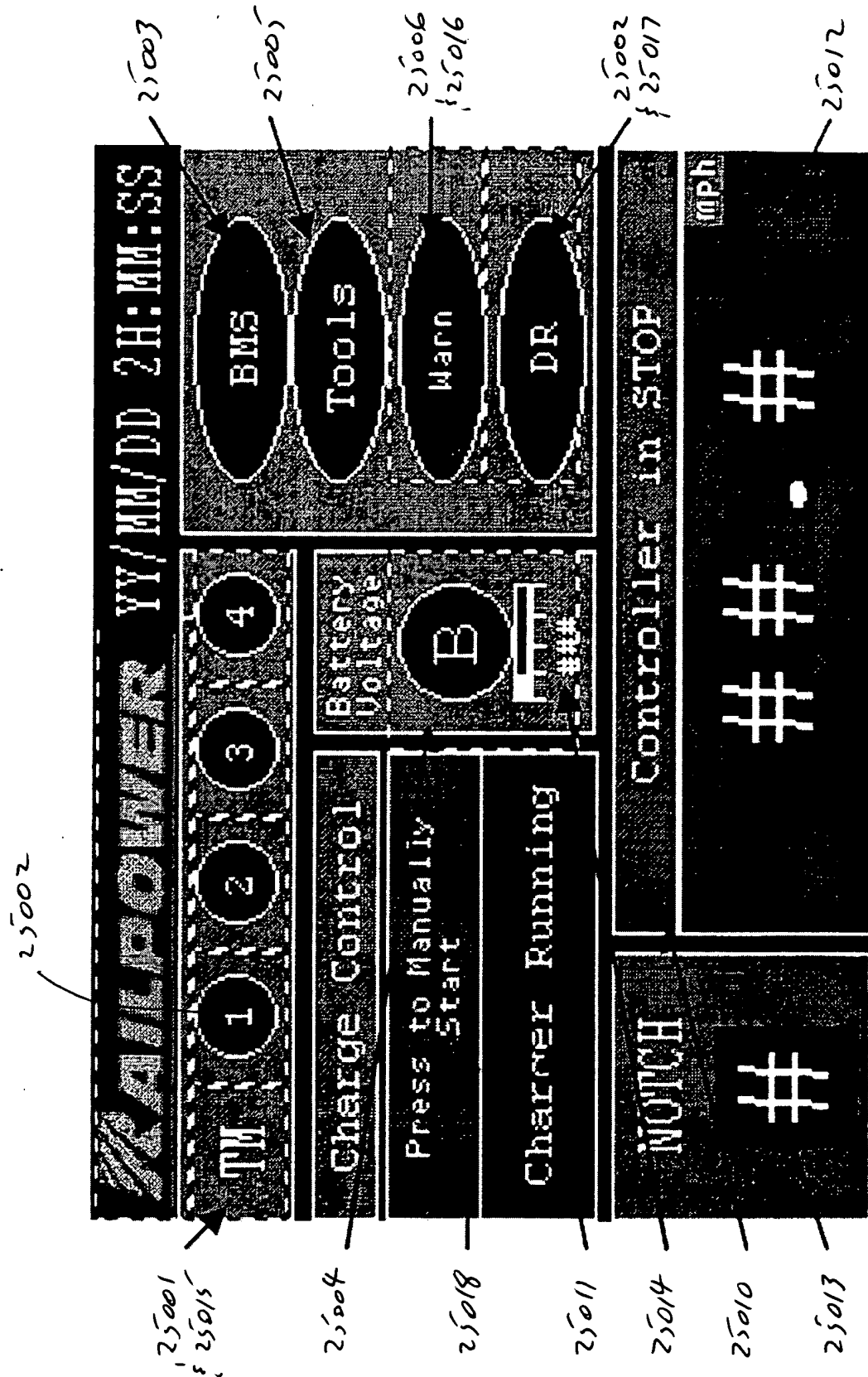


FIG. 25

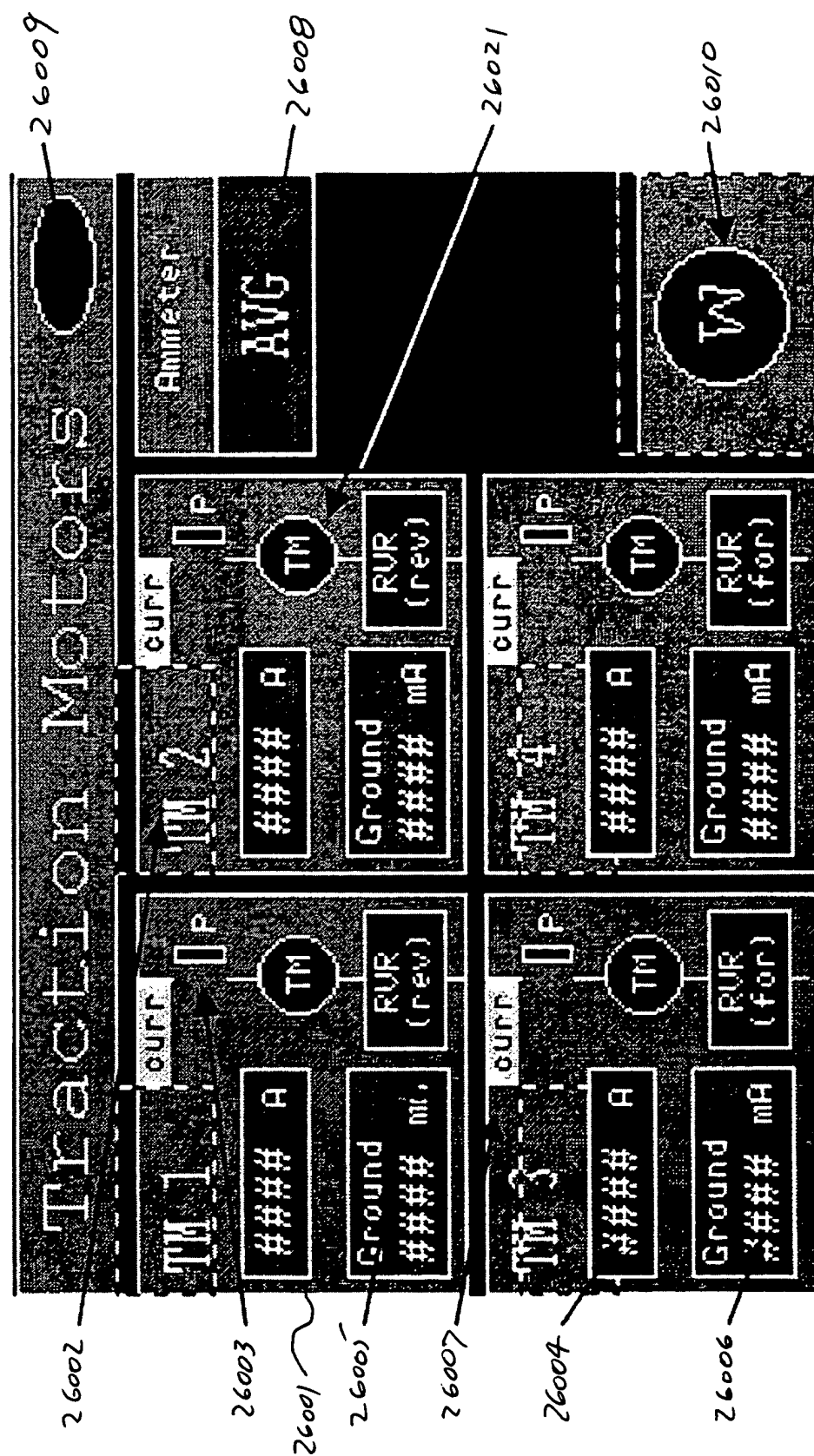


FIG. 26

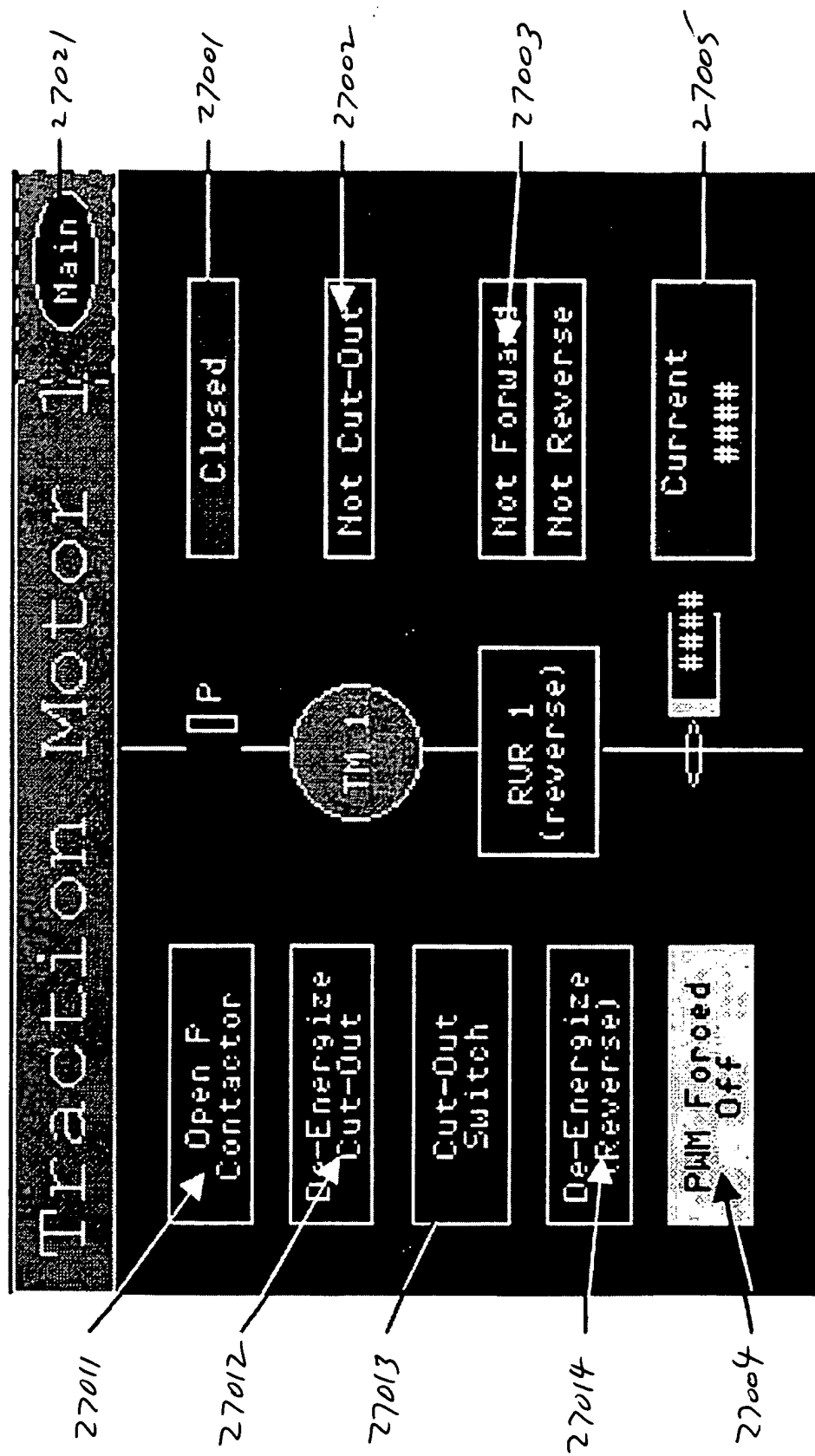


FIG. 27

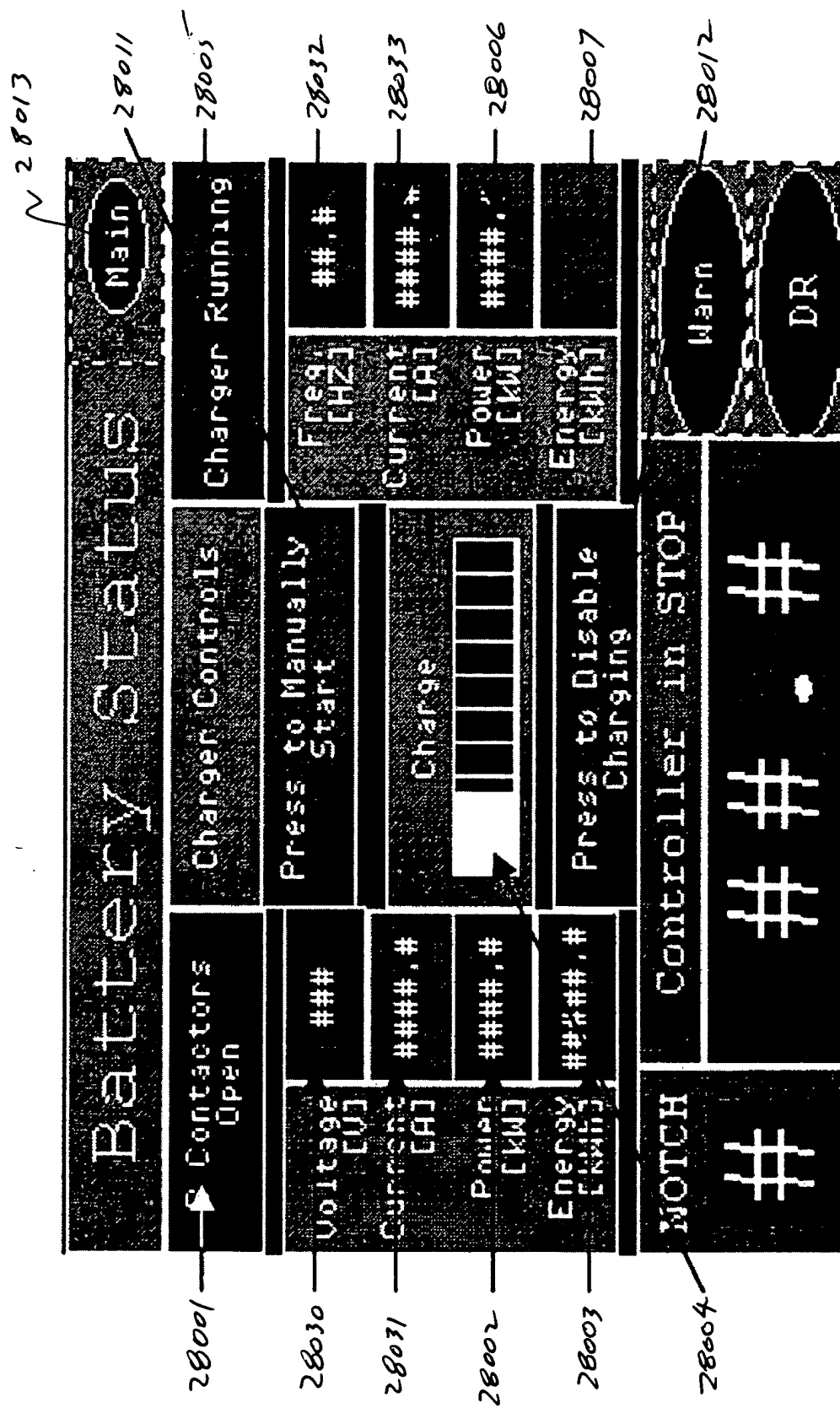


FIG. 28

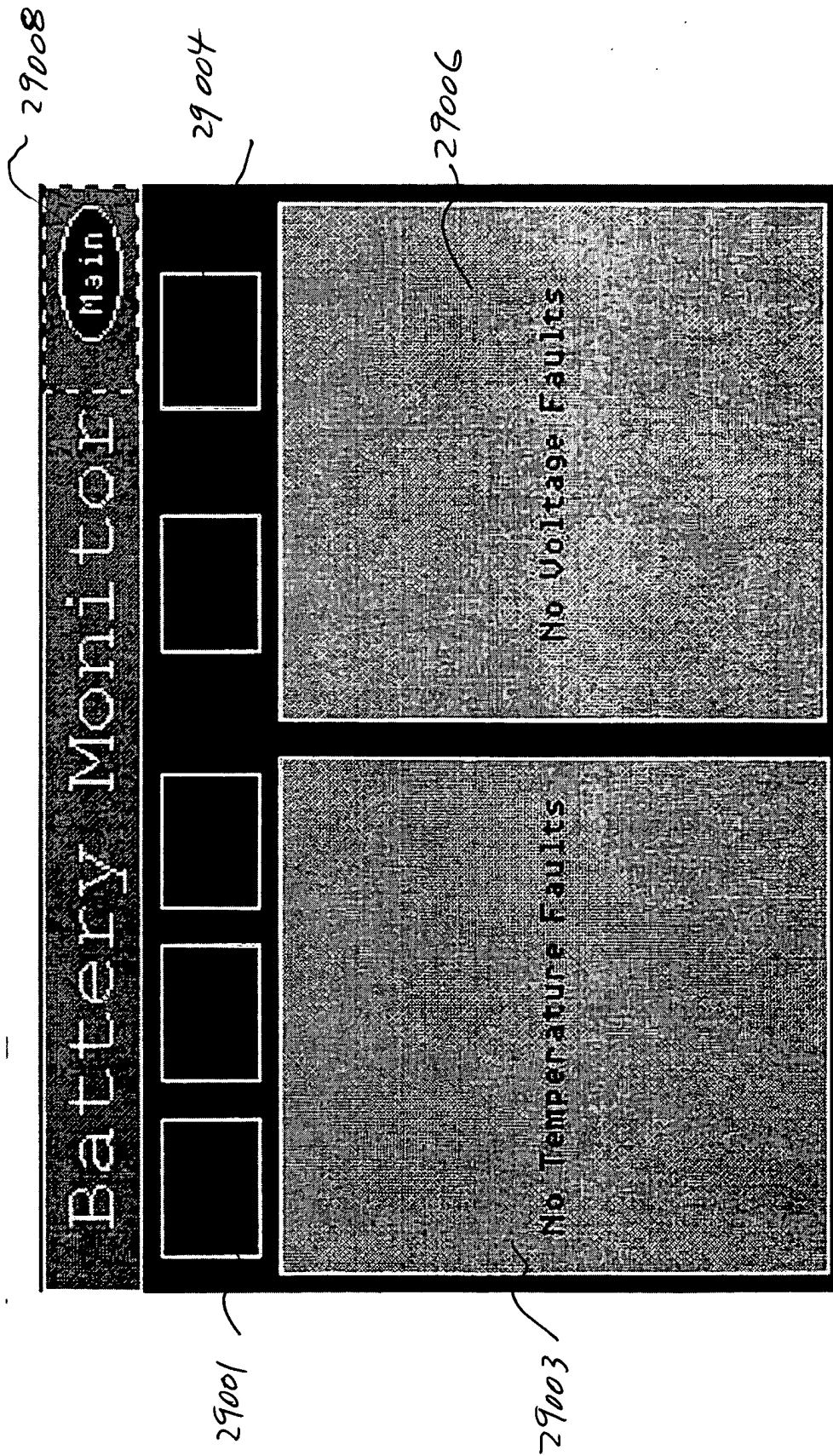


FIG. 29

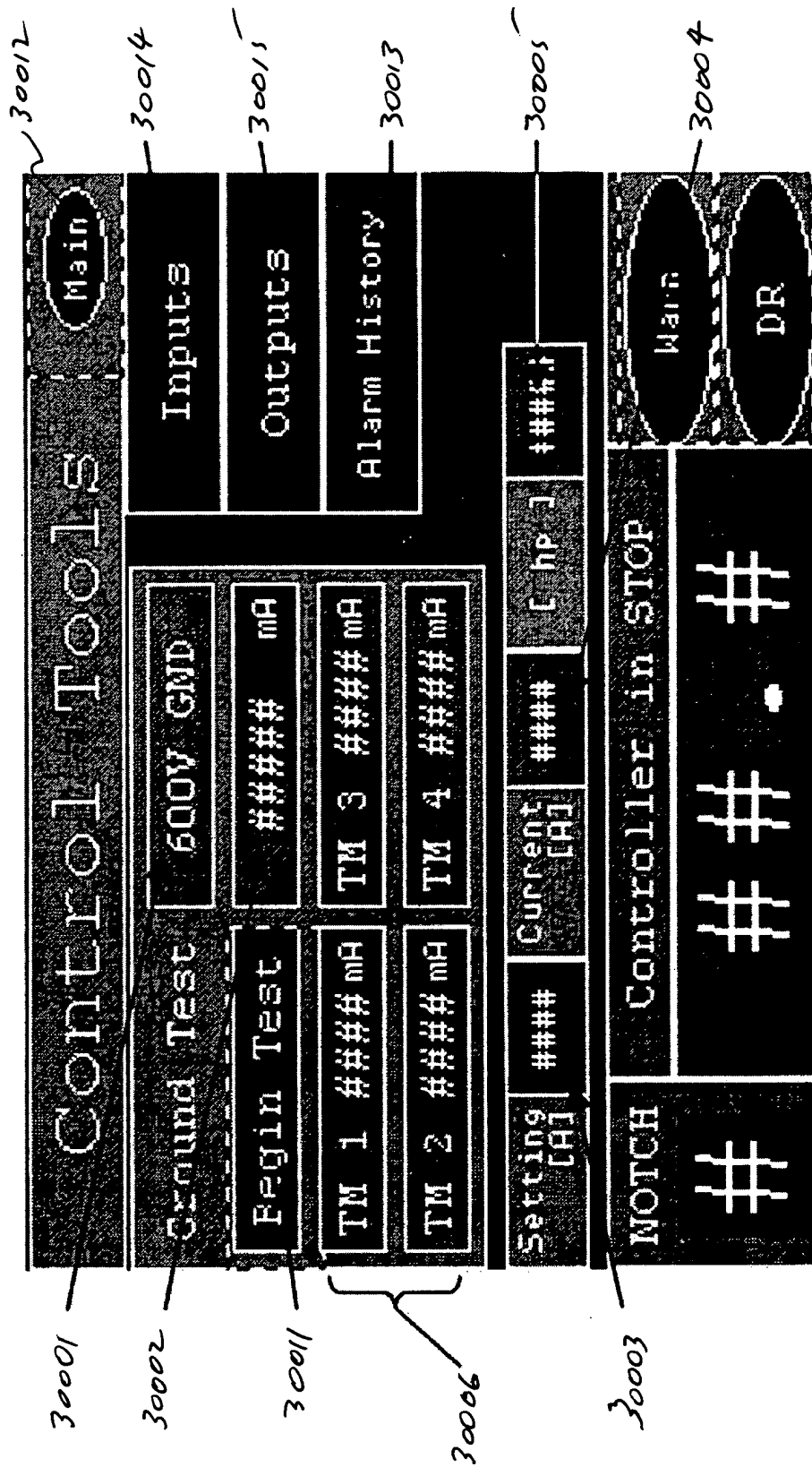


FIG. 30

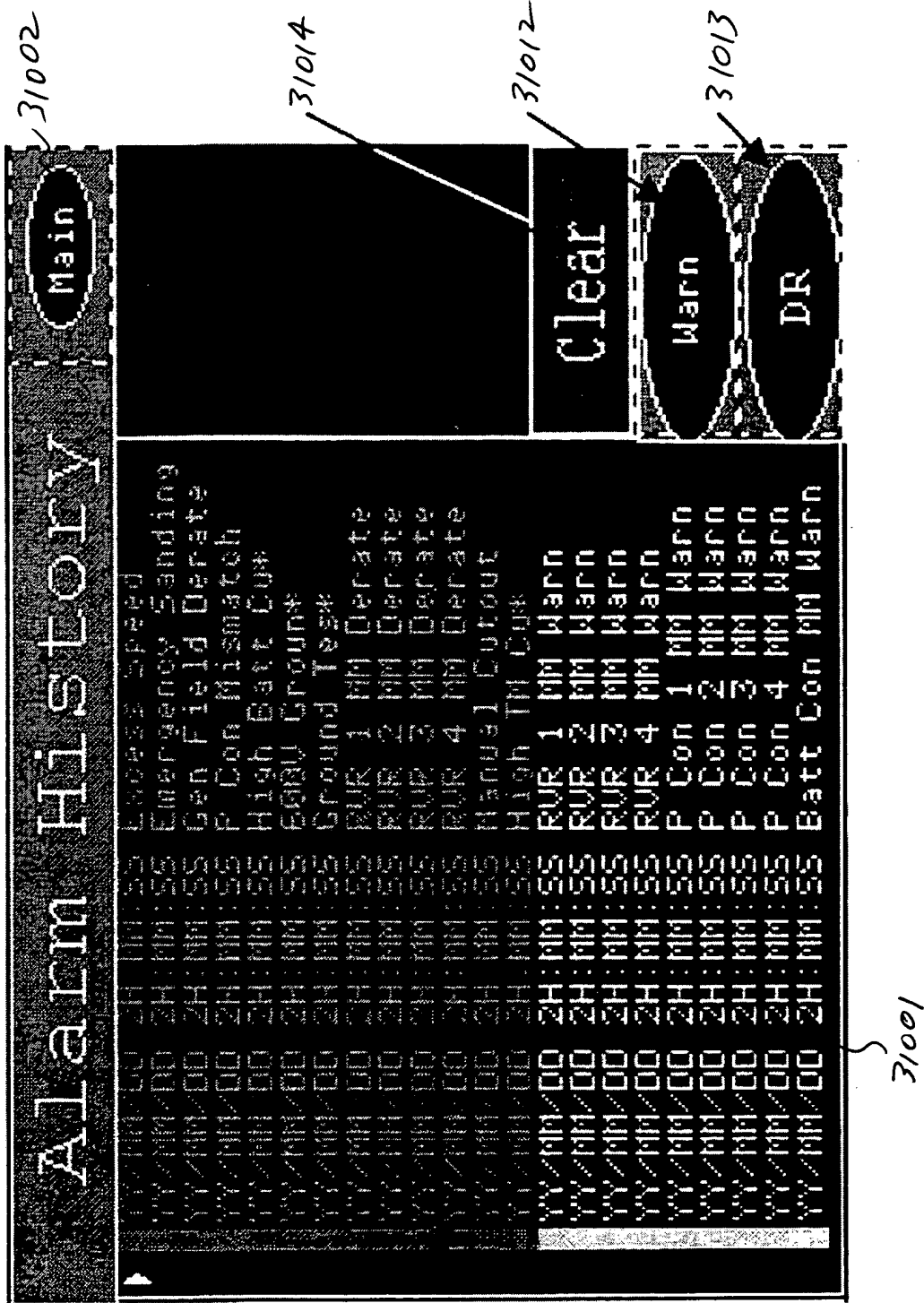


FIG. 31

32001

|              |              |              |           |
|--------------|--------------|--------------|-----------|
| I30=0 NO FCS | I40=0 BOPEN  | I50=0 TM1FWD | Main      |
| I32=0 ID     | I42=0 TM1CO  | I51=0 TM2FWD |           |
| I33=0 DV     | I43=0 TM2CO  | I52=0 TM3FWD |           |
| 0 SANDMAN    | I44=0 TM3CO  | I53=0 TM4FWD |           |
| I35=0 NO ESS | I45=0 TM4CO  | 54=0 TM1OPEN | Tools     |
| I36=0 GF     | I46=0 TM1RUR | 55=0 TM2OPEN |           |
| I37=0 CV     | I47=0 TM2RUR | 56=0 TM3OPEN |           |
| I38=0 FD     | I48=0 TM3RUR | 57=0 TM4OPEN |           |
| I39=0 RE     | I49=0 TM4RUR | 0 CAPSHORT   | Output    |
| I310=0 ER    | 410=0 C1STOP | I59 = 0      |           |
| I311=0 ERS   | I411 = 0     | I510 = 0     |           |
| I312=0 EV    | I412 = 0     | I511 = 0     |           |
| I313=0 FUEL  | I413 = 0     | I512 = 0     | PUM Reset |
| I314=0 ISRUN | 0 CAP5CHRGD  | I513 = 0     |           |
| I315=0 AV    | I415 = 0     | I514 = 0     |           |

32002

32003

32004

FIG. 32



33001

|                       |                        |                       |                       |                 |
|-----------------------|------------------------|-----------------------|-----------------------|-----------------|
| 0:1/0 = 0<br>FWDTM1   | 0:1/8 = 0              | 0:2/0 = 0<br>FWDTM2   | 0:2/8 = 0             | Tools           |
| 0:1/1 = 0<br>RURTM3   | 0:1/9 = 0              | 0:2/1 = 0<br>RURTM4   | 0:2/9 = 0             | Input           |
| 0:1/2 = 0<br>COTM1    | 0:1/10 = 0<br>RSMU     | 0:2/2 = 0<br>COTM2    | 0:2/10 = 0<br>FSMU    | Outputs Enabled |
| 0:1/3 = 0<br>COTM3    | 0:1/11 = 0<br>WS       | 0:2/3 = 0<br>COTM4    | 0:2/11 = 0<br>CI      | 33011           |
| 0:1/4 = 0<br>CLOSEB   | 0:1/12 = 0<br>ERGG     | 0:2/4 = 0<br>COILB    | 0:2/12 = 0            | 33012           |
| 0:1/5 = 0<br>CLOSEGTR | 0:1/13 = 0<br>AB       | 0:2/5 = 0<br>FIELDARM | 0:2/13 = 0<br>GEN     | 33013           |
| 0:1/6 = 0<br>CLOSETM1 | 0:1/14 = 0<br>SA       | 0:2/6 = 0<br>CLOSETM2 | 0:2/14 = 0<br>CHRGAPS |                 |
| 0:1/7 = 0<br>CLOSETM3 | 0:1/15 = 0<br>BLEEDAIR | 0:2/7 = 0<br>CLOSETM4 | 0:2/15 = 0<br>CEPUMP  |                 |
|                       |                        |                       | 0:8/0 = 0<br>XI       |                 |
|                       |                        |                       | 0:8/1 = 0<br>CRRCT1   |                 |
|                       |                        |                       | 0:8/2 = 0<br>CRRCT2   |                 |
|                       |                        |                       | 0:8/3 = 0<br>CRRCT3   |                 |
|                       |                        |                       | 0:8/4 = 0<br>CRRCT4   |                 |

FIG. 33

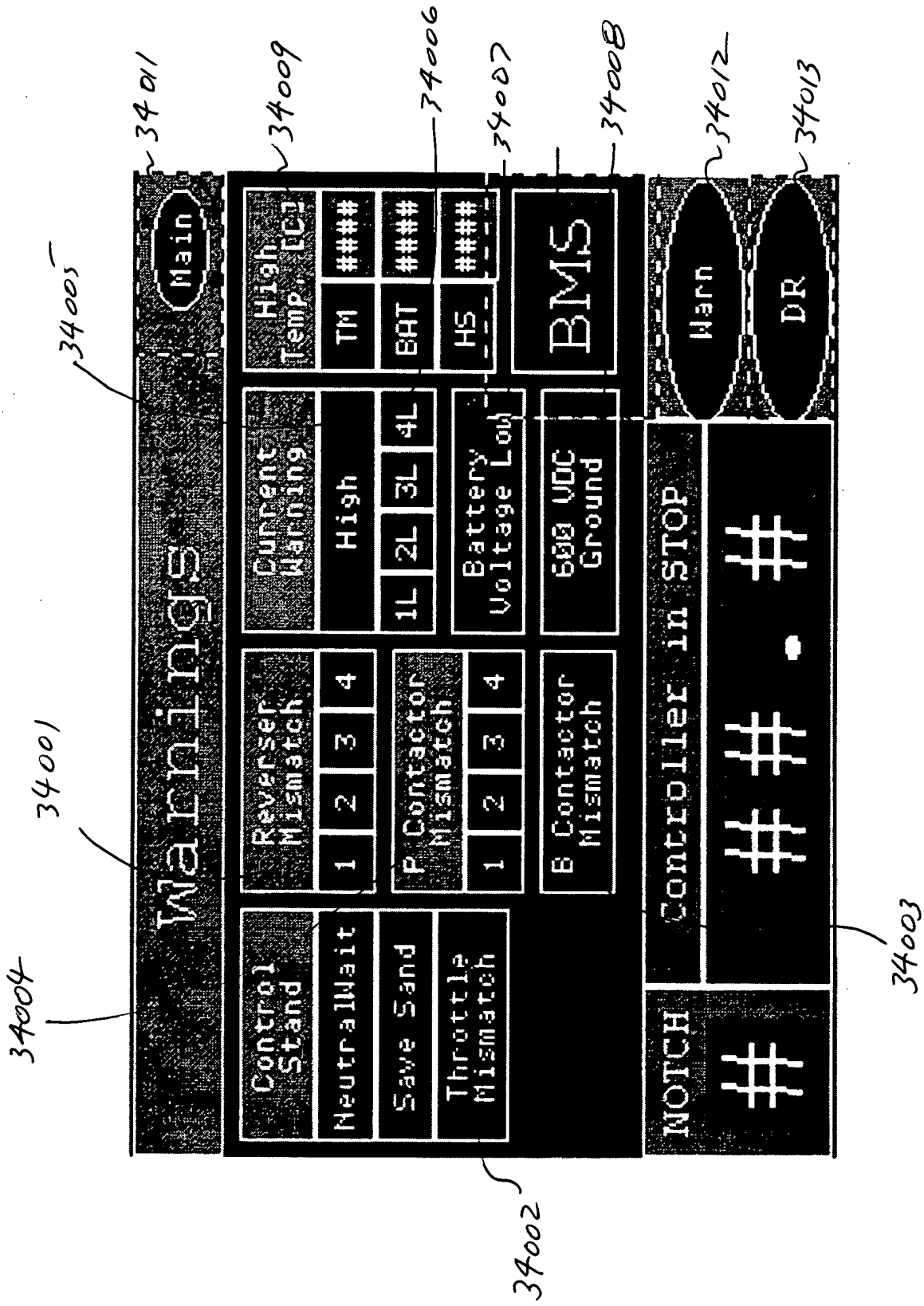
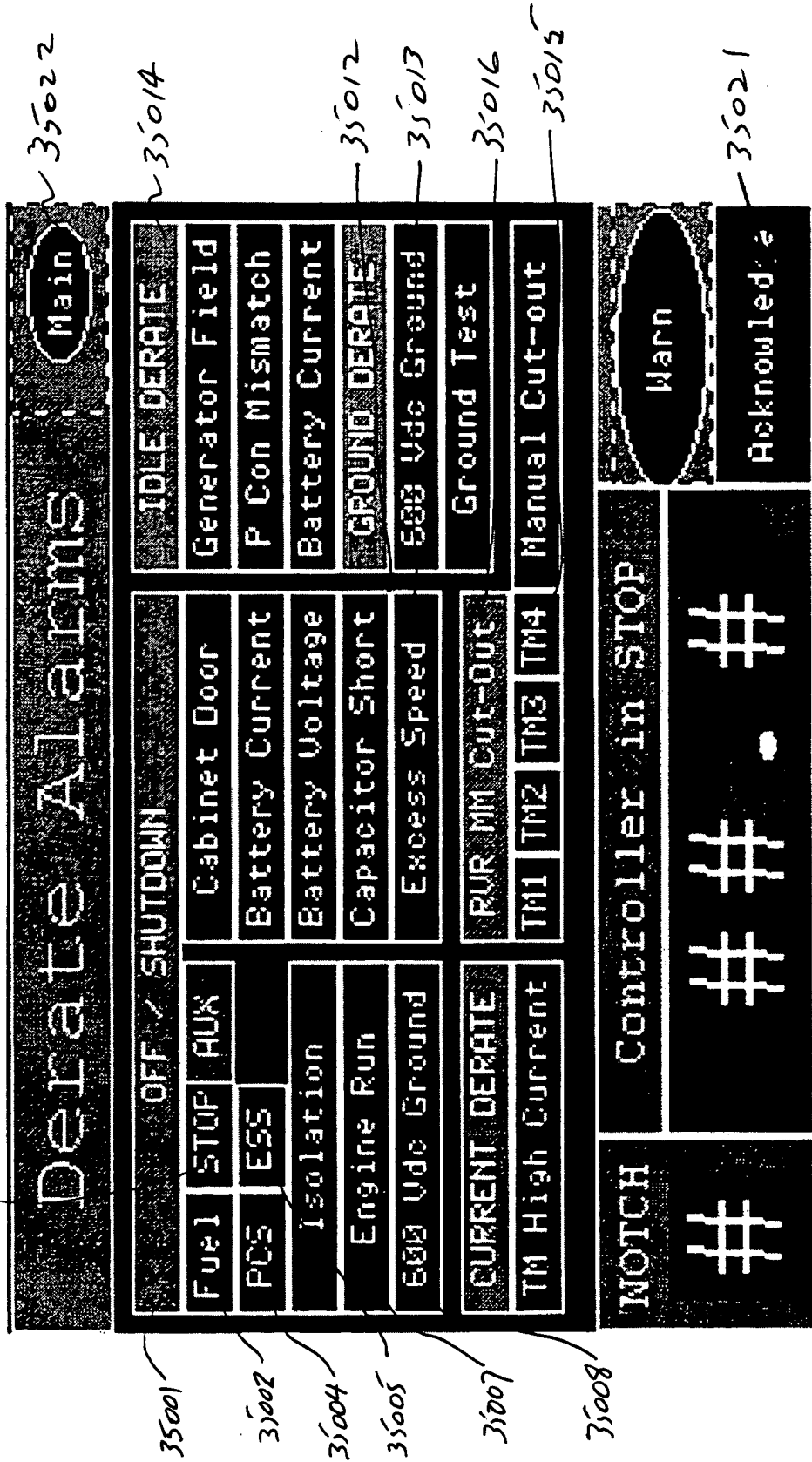


FIG. 34

35003



35001

35002

35004

35005

35007

35008

Fig. 35